

12 V ⊖ •

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@B 4822 725 25473







#### **GENERAL**

This supplement manual should be used together with the 22RC026/00 Service Manual, service 12nc: 4822 725 25470.

Only those parts which differ from those of the 22RC026 are described in this manual.

For the following parts: Technical specifications, Service, Functional explanation, Notes regarding repairs, (Detailed) disassembly procedure, IC circuit descriptions/block diagrams, Circuit diagrams and Trouble shooting refer to the 22RC026 manual.

## **OVERVIEW OF DIFFERENCES**

The differences of the 22RC032/00 with respect to the 22RC026/00 are:

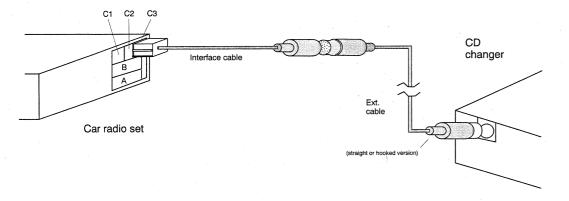
- The 22RC032 is suitable for magazines for 10 discs.
  Note: the radio set which is connected to the 22RC032 should have the feature to select 10 CD's.
  Other sets with CD changer interface can control discs 1 6 only!
- For the 22RC032 a new interface cable is provided for connecting between connector C3 (8 pin C3 part of the new 20-pole C connector) and a 13-pole Hosidin connector. For the extension cable the same cable type as for the 22RC026 is used.

See figure below and the next page.

Note: the other interface cable (C10 - 13p. Hosidin) can <u>also</u> be used (when a radio set with a 10-pole C connector is used).

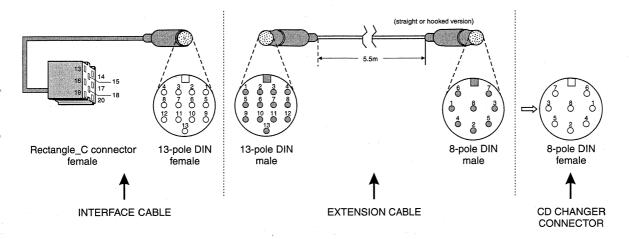
- (See also page 22 of 22RC026 manual) IC801-p.50 is connected to +5V via R835 (47k) i.s.o. GND. ('10CD select' enabled when this pin is high).
- A different connector pcb for the DIN connector (CS505). For completeness *all* pcb lay-outs are inserted in this supplement.
- Difference between some cabinet and mechanism parts. That implies this supplement contains an adapted drawing for the cabinet disassembly procedure, besides complete mechanism and cabinet exploded views.

#### INTERFACE CABLE CONNECTIONS



## **INTERFACE CABLE CONNECTIONS (continued)**

The figure below shows the pin lay-out of the connections of the new Rect\_C3 (8p.) interface cable (type **EA6260** - service 12nc **4822 320 11871**) and the extension cable (service 12nc: **4822 321 62671**). All connections here are seen from the *front* side of the connectors.



#### RECTANGLE\_C3 CONNECTOR

Pin	Signal
13	D <sup>2</sup> B +
14	D <sup>2</sup> B -
15	BUS_GND
16	+ 12V PERM.
17	+ 12V SW.
18	SIGN_GND
19	L_CH
20	R_CH
(SHIELD)	(GND)

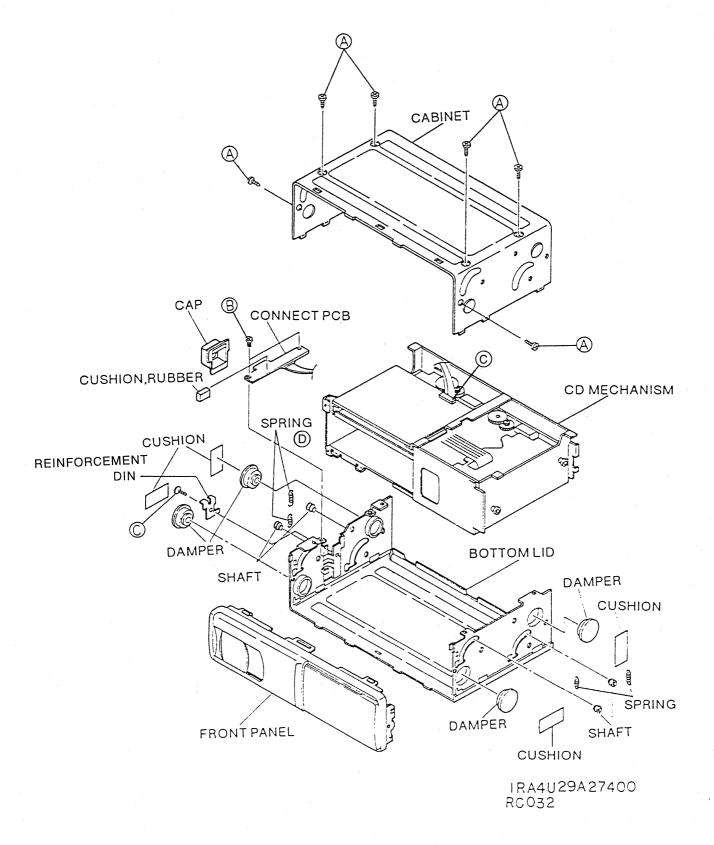
#### 13-POLE DIN CONNECTOR

Pin	Signal
1	SIGN_GND
2	L_CH
3	SIGN_GND
4	R_CH
5	GND
6	N.C.
7	+ 12V SW.
8	+ 12V PERM.
9	BUS_GND
10	N.C.
11	D²B +
12	D²B -
13	N.C.
SHIELD	GND

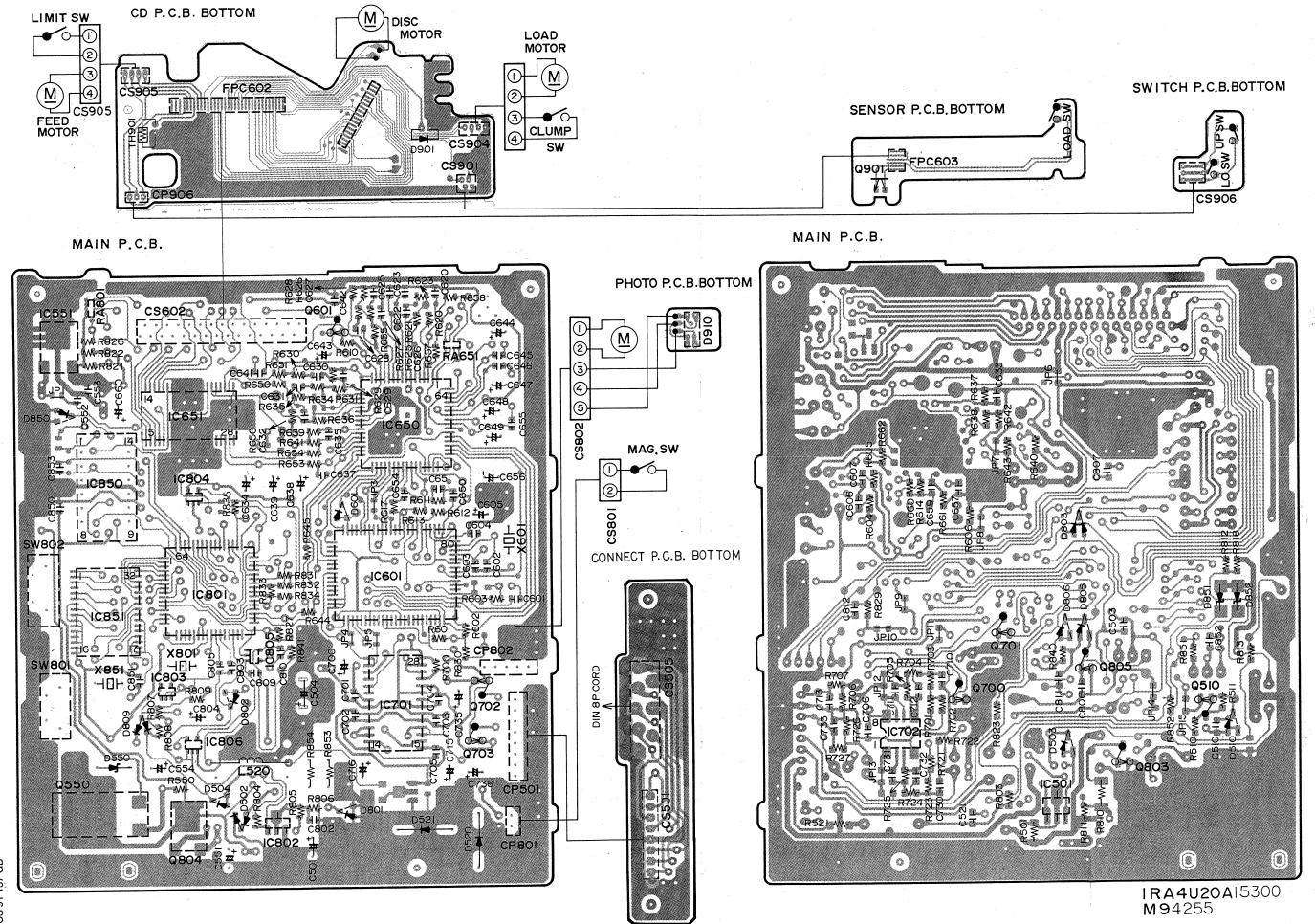
#### 8-POLE DIN CONNECTOR

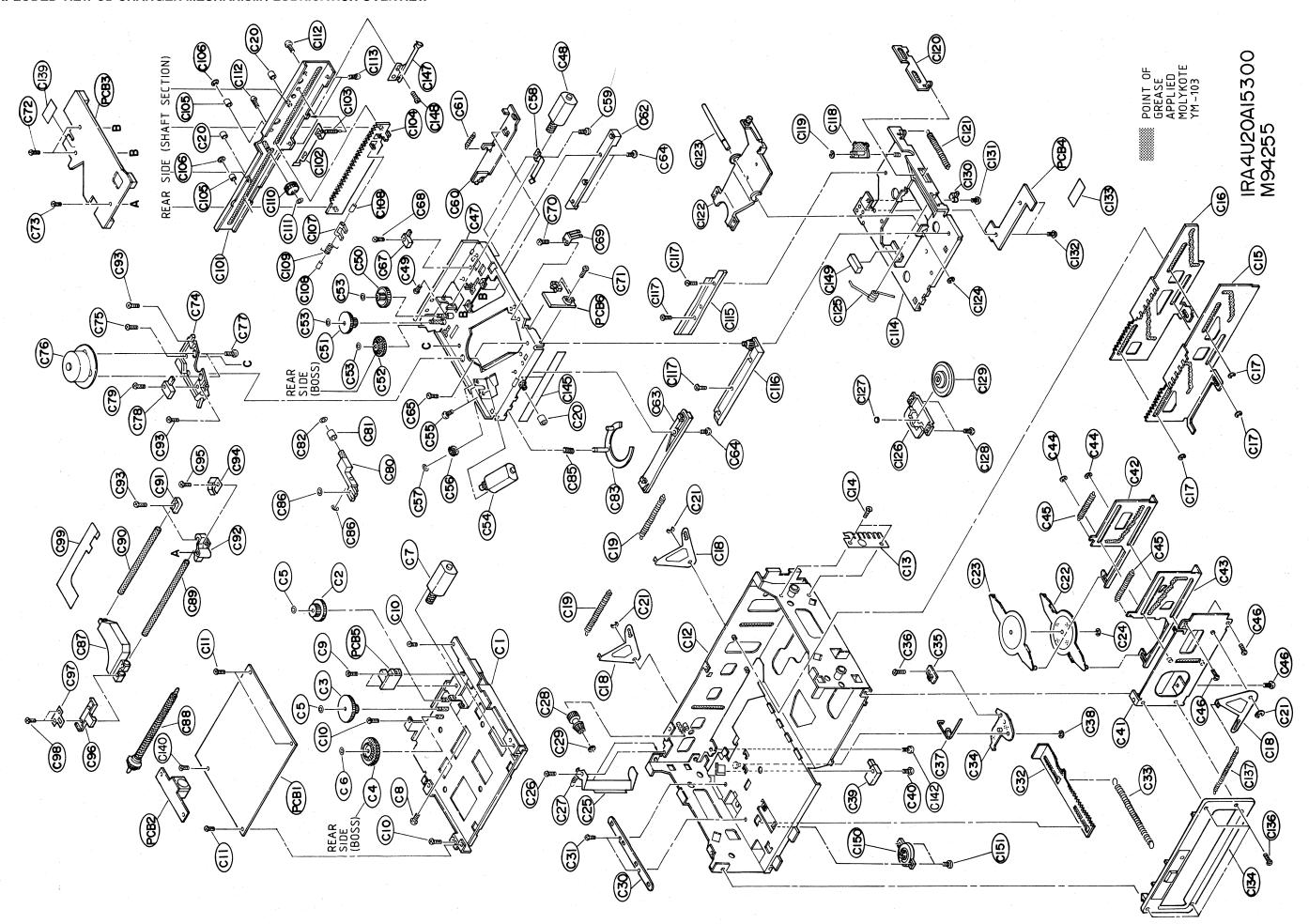
Pin	Signal
1	+ 12V SW.
2	+ 12V PERM.
3	GND
4	D²B -
5	D2B +
6	R_CH
7	L_CH
8	SIGN_GND
SHIELD	GND

- 1. Remove 6 screws (A) which secure the cabinet.
- 2. Remove the cabinet with pushing the front panel stopper.
- 3. Remove the front panel with pushing the both bottom lid stoppers.
- 4. Remove two screws (B), the re-inforcement and the connector (C). The connector pcb can be removed then.
- 5. Remove the cushion.
- 6. Remove the damper.
- 7. If the two springs ① installed between the mechanism and the chassis are removed, the complete mechanism can be removed then.



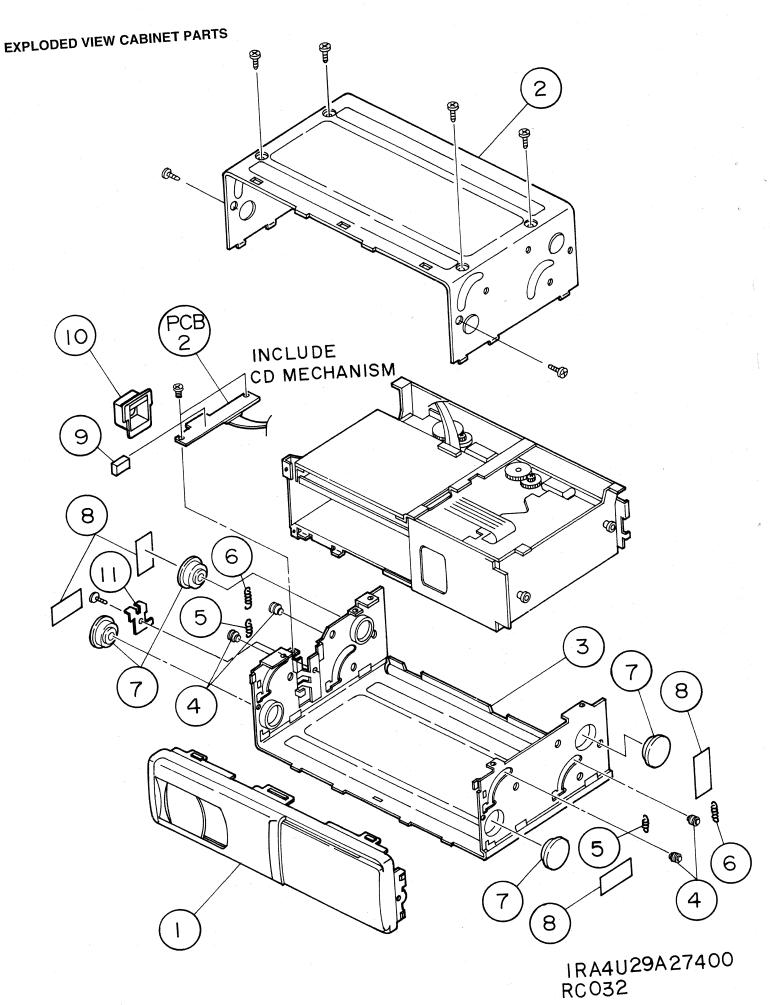
CS 91 136 GB





MECHANICAL	PARTS RC032		
C2	4822 522 33508	Gear, A	
C3	4822 522 33509	Gear, B	
C4	4822 522 33511	Gear, C	
C5	4822 532 12294	Special washer	
C6	4822 532 12295	Special washer	
	4022 002 12200	opoolal washel	
C7	4822 361 30441	Assy DC motor	
C17	4822 532 12293	Special washer	
C28	4822 466 11476	Gear, plate	
C29	4822 532 12295	Mount-M special washer	r
C39	4822 276 13547	Switch, push	
009	4022 270 10047	Owner, pasir	
C44	4822 532 12293	Special washer	
C48	4822 361 30441	Assy DC motor	
C50	4822 522 10595	Gear, LA	
C51	4822 522 10596	Gear, LB	
C52	4822 522 33514	Gear, G	
032	4022 322 33314	dear, d	
C53	4822 532 12294	Special washer	
C54	4822 361 10935	Assy CD feed motor	
C56	4822 522 10597	Gear, FB	
	4822 532 10397		
C57		Special washer	
C67	4822 276 13547	Switch, push	
C76	4822 361 10936	Assy DC motor	
C78	4822 276 13547	Switch, push	
C82	4822 532 12294	Special washer	
C86	4822 532 12295	Special washer	
C87	4822 691 10567	Pickup unit	
C67	4022 091 10307	rickup unit	
C91	4822 535 10449	Shaft	
C92	4822 502 14399	Screw	
C96	4822 463 11123	Thrust, screw	
	4822 403 11123		
C102		Lever, switch	
C104	4822 402 10693	Lever assy - slide/load	
C106	4822 532 12293	Special washer	
		Special washer	
C110	4822 522 33515	Gear, H	
C111	4822 532 12294	Special washer	
C129	4822 528 11071	Flange	
C130	4822 276 13547	Switch, push	
0404	4000 404 40074	Frank namel for social and	
C134	4822 464 10274	Front panel for mechanis	SIN
	4822 321 62668	Cable, 2p - 160mm	
	4822 320 11737	Cable, 4p - 40mm	
	4822 320 11738	Cable, 5p - 90mm	
	· · · · · · · · · · · · · · · · · · ·		

Note: all parts not mentioned here are NO service parts!



## **CABINET PARTS AND ACCESSORIES**

Note: all parts not mentioned here are NO service parts!

1	4822 459 04503	Front panel assy
2	4822 442 00795	Cabinet assy
3	4822 442 00796	Bottom assy
7	4822 529 10308	Damper
10	4822 462 10841	Cap for DIN connector
	4822 736 15096	Instruction manual
	4822 402 10694	Accessory part assy - left/right brackets
	4822 310 10946	Accessory part assy - bracket & adhesive tape
	4822 310 10778	Accessory part assy - brackets and bolts
	4822 321 62671	DIN extension cable 5.5m
	4822 321 62263	Rect_C10 - DIN interface cable
	4822 320 11871	Rect C3 - DIN interface cable EA6260
		<del></del>
	4822 691 10578	Magazine assy
	4822 502 21556	Transport screw

## **ELECTRICAL PARTS**

Note: all parts not mentioned here are NO service parts!

			0001	4000 400 40707	0041000.0
RC032		·	Q601	4822 130 10787	2SA1338-6
			Q700	4822 130 90323	DTC114TK
<b>→</b> ⊢			Q701	4822 111 90813	UN2113
		500010	Q702	4822 130 90323	DTC114TK
D502	4822 130 82557	DCC010	Q703	4822 130 90323	DTC114TK
D503	4822 130 10782	DSB015			
D504	4822 130 83633	MA704A	Q803	4822 130 63551	DTC124XK
D510	4822 130 83765	Zener 5.6V	Q804	4822 130 62912	2SB1202(ST)
D520	4822 130 31533	DS135	Q805	4822 130 10785	2SA1179-M6
			Q901	4822 130 63655	Photo diode PT4850F
D521	4822 130 31533	DS135			
D550	4822 130 10783	Zener 8.2V	200000		
D601	4822 130 83631	DSB010	000000		
D801	4822 130 83632	Zener 5.1V	IC501	4822 209 33241	MC147805AUT
D802	4822 130 83631	DSB010	IC551	4822 209 33094	L78M05T
			IC601	4822 209 15225	LC78620E-D
D803	4822 130 83637	DCB010	IC650	4822 209 15227	LA9240M servo
D805	4822 130 83637	DCB010	IC651	4822 209 15228	BA6999FP
D806	4822 130 83631	DSB010			
D809	4822 130 10784	DCA010	IC701	4822 209 15226	PCM1710U
D850	4822 130 80273	Zener 8.2V	IC702	4822 209 30455	NJM2100M
			IC801	4822 209 15229	LC66566B-4H90
D851	4822 130 10657	Zener 5.6V	IC802	4822 209 63631	S-8054HN-CB
D852	4822 130 10657	Zener 5.6V	IC803	4822 209 15203	PST9138N
D901	4822 130 10114	LED SLR-989A-AB			
D910	4822 130 83766	Photo coupler GP1S51V	IC804	4822 209 32984	TC7SU04F
20.0	1022 100 00.00		IC805	4822 209 32984	TC7SU04F
Ø			IC806	4822 209 72397	TC4S71F
€			IC850	4822 209 33758	LB1644
Q510	4822 130 60753	2SC2812-L6	IC851	4822 209 32743	MSM6307GS
~~.	4822 130 10786	2SD2199S-TSD-DR			

## **MISCELLANEOUS**

CS505	4822 267 31758	DIN socket
L520	4822 157 11124	SK 5mH
SW801	4822 276 13828	Eject switch
SW802	4822 276 13548	Reset switch
TH901	4822 111 92201	Thermistor
		NTH4G42B104EB
X601	4822 242 81702	Crystal 16.9344MHz
X801	4822 242 73769	Ceramic resonator
		4.190MHz
X851	4822 242 10678	Ceramic resonator
		5.75MHz
	4822 276 13547	UP switch
	4822 276 13547	LOW switch





PHIL-05230



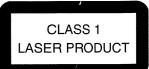
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#### **SPECIFICATIONS**

13.2VDC (nom.) Power supply

Distortion 16.0VDC (max.) Data - Track mute  $\leq$  0.01% (at 1kHz) ≥ 93dB (A-weighted)

Frequency response

20-20,000 Hz (± 1dB)

Wow and flutter unmeasurable 700mV Output voltage

DAC resolution

1 bit per channel

Operating positions

Bus interface

D<sup>2</sup>B

: Horizontal/Vertical/45°

Crosstalk L<>R

≤ -75dB

Weight 2 kg

S/N ratio

96dB (A-weighted)

Dimensions (HxWxD) 66 x 259 x 175 mm

#### **SERVICE**

**!! IMPORTANT IMPORTANT!!** 

When disassembling, ALWAYS protect the pick-up unit against ESD damage by closing the solder connection of the pick-up unit either on the CD panel or the flexible foil pcb!

(See also the repair notes and disassembly procedures described in this manual.) New pick-up units are supplied with CLOSED solder connections on the flexible foil pcb.

Do NOT forget to remove the solder connection AFTER replacing the unit!

#### **SERVICE HINTS**

To prevent magazine check (duration approx. 30-40 sec.), insert the magazine and press the 'eject' button SIMULTANEOUSLY. In that case disc no.1 is loaded and the set comes into 'Standby' mode.

Use (home-made) extension cables of approx. 30 cm to get access to the bottom side of the main pcb for measuring purposes. Two extension cables (a 2-pole and a 5-pole one) are required. To make these cables, the following parts can be ordered and used for making the extension cables:

CS801 - cable assy 2-pole - 4822 321 62668

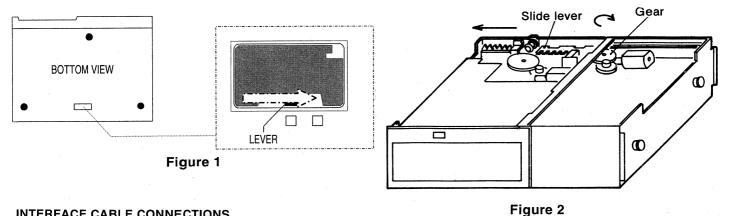
CS802 - cable assy 5-pole - 4822 320 11738

The set can also operate without cover, but take care of the following measures:

- ABSOLUTELY NEVER PERMIT LASER BEAMS TO ENTER THE EYES

DO NOT EXPOSE THE SET TO BRIGHT (SUN)LIGHT

When the CD changer has a defect while the magazine is still in, the magazine can be removed by moving the emergency eject lever to the right by a small screwdriver, as shown in figure 1 below. First the protection sheet has to be removed. DO NOT FORGET to put the sheet back, to prevent dust intrusion! When a disc is chucked, turn the gear to the right until the slide lever reaches to the edge of the left side (see figure 2).

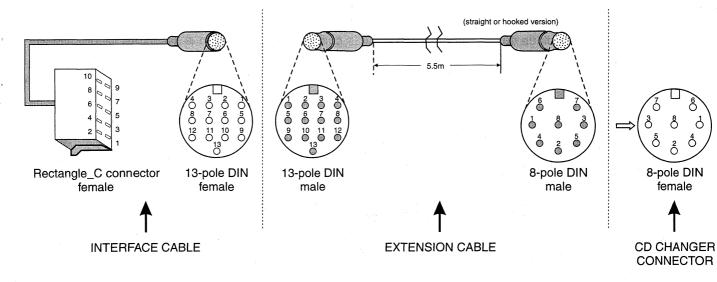


#### INTERFACE CABLE CONNECTIONS

## **INTERFACE CABLE CONNECTIONS (continued)**

The figure below shows the pin layout of the connections of the interface cable (service 12nc: **4822 321 6263**) and the extension cable (service 12nc: **4822 321 62671**).

All connections here are seen from the front side of the connectors.



### RECTANGLE\_C CONNECTOR

Pin	Signal
1	BUS_GND
2	D <sup>2</sup> B +
3	D <sup>2</sup> B -
4	N.C.
5	+ 12V PERM.
6	GND
7	+ 12V SW.
8	R_CH
9	L_CH
10	SIGN_GND
(SHIELD)	(GND)

## 13-POLE DIN CONNECTOR

Pin	Signal
1	SIGN_GND
2	L_CH
3	SIGN_GND
4	R_CH
5	GND
6	N.C.
7	+ 12V SW.
8	+ 12V PERM.
9	BUS_GND
10	N.C.
11	D <sup>2</sup> B +
12	D²B -
13	N.C.
SHIELD	GND

#### 8-POLE DIN CONNECTOR

Pin	Signal
1	+ 12V SW.
2	+ 12V PERM.
3	GND
4	D2B -
5	D2B +
6	R_CH
7	L_CH
8	SIGN_GND
SHIELD	GND

#### **FUNCTIONAL EXPLANATION**

#### **FUNTIONAL EXPLANATION**

#### 1. APC (Auto laser power control)

This is the circuit to control laser power of pickup, and laser ON/OFF is set by command from micro computer.

### 2. RF Amplifier (I pattern output)

This inputs pickup photo diode output current (A+C) to FIN2 (1pin), and (B+D) to FIN1 (2pin). Input current is IV-converted, and output to RFSM (41pin) of RFSM amplifier output by way of AGC circuit. Self-contained AGC circuit has variable range of  $\pm 3$ dB, and the time constant can be varied by outside-fixed condenser of PH1 (60pin). This also controls bottom level of EFM signal (RFSM output), and this response can be varied by outside-fixed condenser of BH1 (61pin). Central gain of AGC variable range is set by resistance value between RFSM (41pin) and RFS-(42pin).

#### 3. SLC (Slice level control)

SLC makes duty of EFM signal which is input to DSP 50%. This judges duty, and decides DC level by integrating EFMO signal which is output from DSP.

#### 4. Focus servo

Focus error signal can be gained by detecting the difference between (A+C) and (B+D), which is (B+D)-(A+C), and it is sent to FE (20pin). Focus error signal gain is set by resistance value between FE (20pin) and FE-(21pin).

FA amplifier is phase compensating Amplifier, and the equalizer curve is set by outside-fixed condenser and resistance. Besides, this amplifier has muting function. FD amplifier has the following functions; phase compensating circuit, focus search signal synthesis, and offset cancelling. Focus search starts by F-SERCH command, and generates lamp waveshape by inner clock. Focus error signal by this waveshape detects infocus condition (focus zero cross), and turns on focus servo. Lamp waveshape amplitude is set by resistance between FD (16pin) and FD-(17pin).

#### 5. Tracking servo

This inputs pickup photo diode output current to E (3pin) and F (4pin). Input current is IV-converted, and output to TE (7pin) by way of VCA circuit for balance adjustment and VCA circuit for follow-up to RFAGC circuit. Tracking error gain is set by resistance between TE-(6pin) and TE (7pin).

TOFF amplifier just after TE (7pin) turns off servo by TOFF signal from DSP.

TH amplifier varies servo response characteristic by TGL signal from DSP. or by THLD signal of inside formation by detecting JP signal. When DEFECT is detected, inside mode changes into THLD. This can be avoided by causing short circuit in DEF (49pin) to "L"=GND. In case of detection, gain can be automatically up by configulating outside DCI (9pin) band pass filter which picks up only shock element out of tracking error signals and putting it in.

TO amplifier has the function of synthesizing JP pulse and cancelling tracking offset. JP pulse is set by JP (14pin) (THLD is detected inside).

#### 6. Sled servo

SLEQ (28pin) sets response characteristic. Amplifier after SLEQ (28pin) has muting function, and mutes SLOF (38pin) by "H" or SLED OFF command. Sled feed is operated by means of current input to SL-(30pin) and SL+(31pin), that is, by connecting to output port of micro computer by resistance and setting feeding gain by the resistance value.

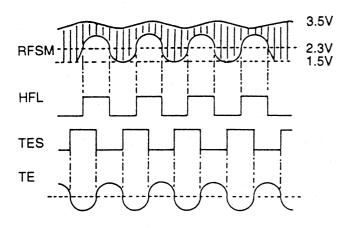
#### 7. Spindle servo

This configurates, together with DSP, servo circuit to keep disc linear velocity at a scheduled level. This receives signal from DSP by CV-(39pin) and CV+(40pin), and sets equalizer characteristic by SP (23pin), SP-(36pin) and SPD (27pin) which output to SPD (27pin). SPG (25pin) is set by resistance with which amplifier gain of 12cm mode is connected to standard voltage.

#### **FUNCTIONAL EXPLANATION**

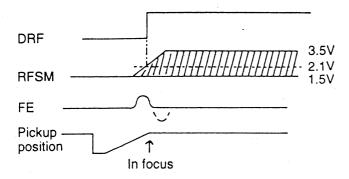
#### 8. TES, HFL (Traverse signal)

In transferring pickup from inner track to outer track, EF output from pickup should be so connected that HFL and TES have phase relation as shown in the figure below. TES comparator has about ±100mV hysteresis at -polarity comparator against TESI input. To pickup exclusively necessary signals out of TE signals, band pass filter is configurated outside.



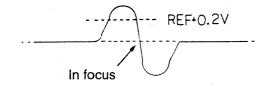
## 9. DRF (Beam level judgement)

DRF becomes "H", when EFM signal (RFSM output) is held at peak value by condenser of PH1 (60pin) and peak value of RFSM gets over 2.1V. Condenser of PH1 (60pin) is related to setting both DRF constant when detected and RFAGC response.



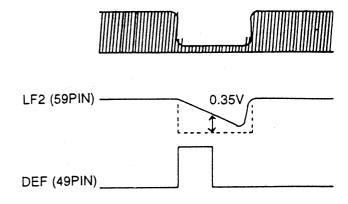
## 10. In-focus judgement

This detects DEF+0.2V of focus error signal S curve, and then judges focus zero cross (being in focus) when S curve becomes REF.



#### 11. DEFECT

This normally maintain mirror surface level by condenser of LF2 (59pin), and when lack of EFM signal (RFSM output) gets over 0.35V, outputs "H" to DEF (49pin). When DEF (49pin) becomes "H", tracking servo changes into THLD mode.

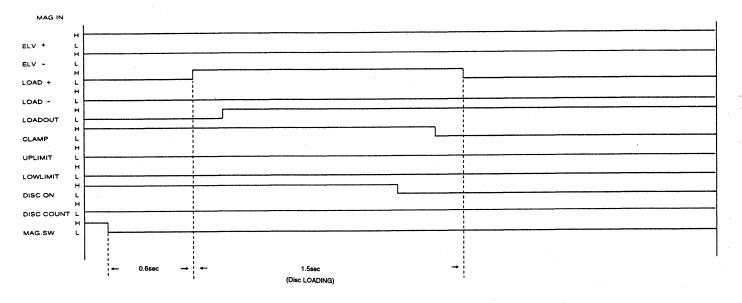


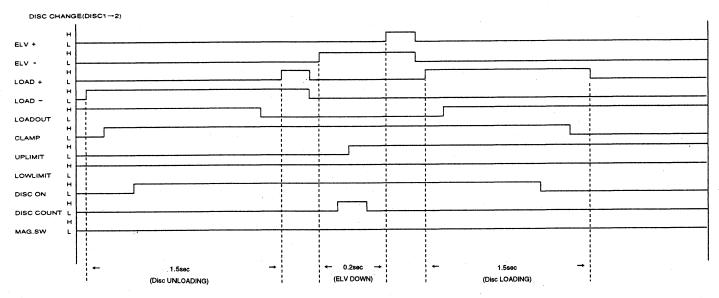
#### 12. Reset circuit

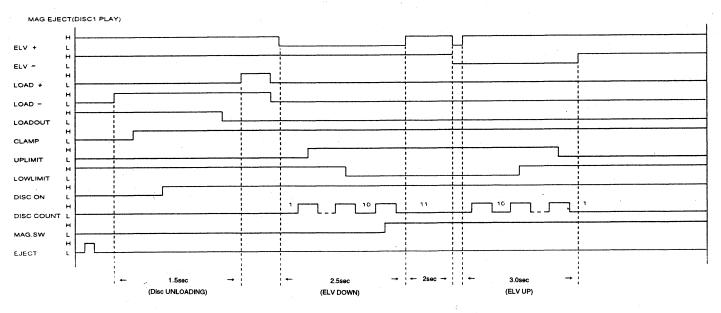
When Vcc gets over about 2.8V, Power on Reset is cancelled.

## **FUNCTIONAL EXPLANATION**

## Mechanical Timing







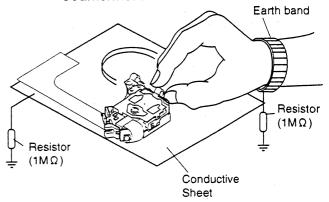
## NOTES REGARDING COMPACT DISC PLAYER REPAIRS

## (1) Preparations

- Compact disc players incorporate a great many ICs as well as the pickup (laser diode). These components are sensitive to, and easily affected by, static electricity. If such static electricity is high voltage, components can be damaged, and for that reason components should be handled with care.
- 2) The pickup is composed of many optical components and other high-precision components. Care must be taken, therefore, to avoid repair or storage where the temperature of humidity is high, where strong magnetism is present, or where there is excessive dust.
- (2) Notes for repair
- 1) Before replacing a component part, first disconnect the power supply lead wire from the unit.
- 2) All equipment, measuring instruments and tools must be grounded.
- The workbench should be covered with a conductive sheet and grounded.
  - When removing the laser pickup from its conductive bag, do not place the pickup on the bag. (This is because there is the possibility of damage by static electricity.)

- 4) To prevent AC leakage, the metal part of the soldering iron should be grounded.
- 5) In removing short circuit solder of LASER PICKUP, use ceramic heater type of soldering iron.
- 6) Workers should be grounded by a earth band  $(1M\Omega)$ .
- 7) Care should be taken not to permit the laser pickup to come in contact with clothing, in order to prevent static electricity changes in the clothing to escape from the earth band.
- 8) The laser beam from the pickup should NEVER be directly facing the eyes or bare skin.

#### Countermeasure of electrostatic

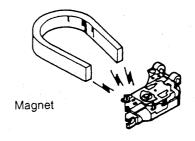


### NOTES REGARDING HANDLING OF THE PICKUP

- (1) Notes for transport and storage
  - 1) In retaining the product, high temperature, high humidity and dusty circumstances must be avoided. After taking it out of packing box, never leave it at the place where dust can occur. (Take every possible preventive means against dust.)
  - 2) The pickup should always be left in its conductive bag until immediately prior to use.
  - 3) As this is minutely adjusted, be careful never give it any shock from drop or careless handling.

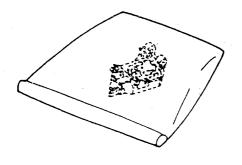
#### (2) Repair notes

1) The pickup incorporates a strong magnet, and so should never be brought close to magnetic materials.



#### Storage in conductive bag







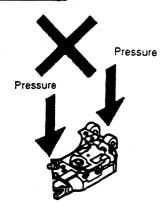
#### NOTES REGARDING HANDLING OF THE PICKUP

2) The pickup should always be handled correctly and carefully, taking care to avoid external pressuer and impact. If it is subjected to strong pressure or impact, the result may be an operational malfunction and/or damage to the printed circuit boaed.



How to hold the pickup

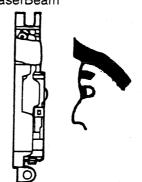
Grabbing print circuit board or pulling connecting wire causes function deterioration or failure. Be sure to hold the whole housing.



- 3) If LASER PICKUP or MECHA ASSY is set or reset with power supplied. LASER DIODE or PHOTO DETEC-TOR is damaged. Be sure to turn off power supply switch before setting or resetting LASER PICKUP or MECHA ASSY.
- 4) To prevent LASER DIODE or PHOTO DETECTOR from being deteriorated or damaged by static electricity, be sure to thoroughly practice earthing as a preventive means against damage from static electricity.
- 5) Take every possible means against damage of LASER DIODE or PHOTO DETECTOR from overcurrent or overvoltage. (Example—Use power source equipped with current limiter.)
- 6) If fingerprint or any other foreign material is attached on objective lens, the function is extremely deteriorated. Be sure never to touch objective lens. Particularly, be very careful in removing or fitting lens cover.
- 7) PICKUP is a single part, and very minutely adjusted as such. Therefore, never touch any of adjustment points, fixing screws or print circuit board of PICKUP.
  - If you touch partially fixed volume soldered on circuit board (beam quantity adjusting volume), emitted beam quantity (RF LEVEL) can change. Never touch partially fixed volume.

- (2) If there occurs even a very small shift in the circuit board fixture position, the function greatly changes. In handling LASER PICKUP, be very careful to hold metal part of housing (HOUSING).
- 8) If metal part of adjusting rod or driver touches circuit board when power is supplied, it can cause failure. Be careful.
- 9) Laser beam may damage the eyes! Absolutely never permit laser beams to enter the eyes! Also NEVER switch ON the power to the laser output part (lens, etc.) of the pickup if it is damaged.

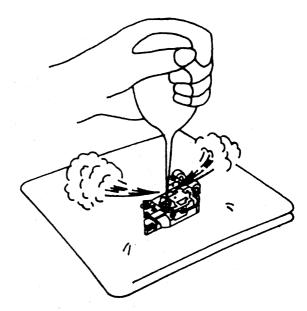
#### LaserBeam



NEVER look directly at the laser beam, and don't let contact fingers or other exposed skin.

10) Cleaning the lens surface

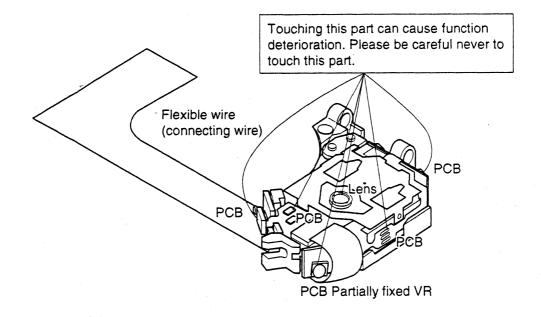
If there is dust on the lens surface, the dust should be cleaned away by using an air brush (such as used for camera lens). The lens is held by a delicate spring.



Conductive Sheet

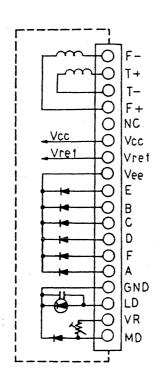
11) Never attempt to disassemble the pickup.

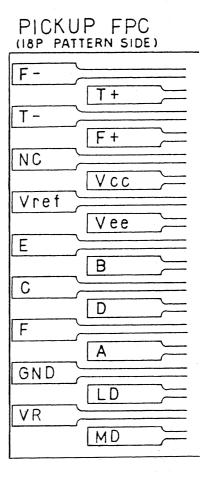
### NOTES REGARDING HANDLING OF THE PICKUP



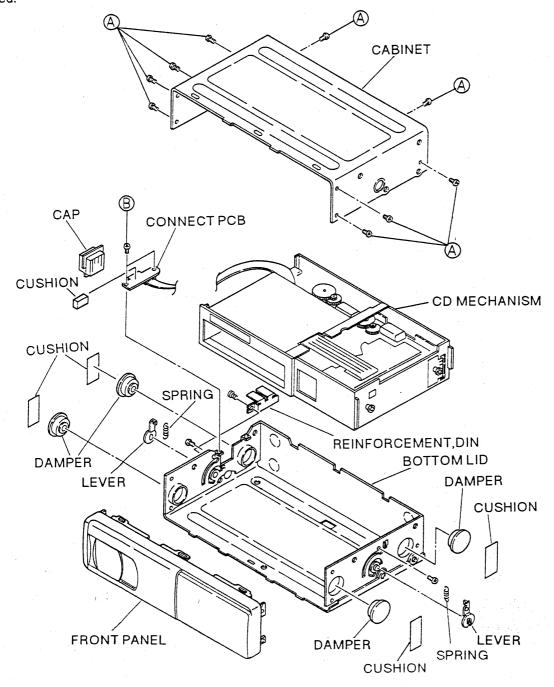
Holding and pulling flexible wire can cause breaking of wire. Please be sure to hold the housing itself in handling

#### **PICKUP DETAIL**



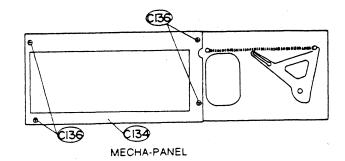


- 1. Remove 9 screws (A) securing the cabinet.
- 2. Remove the cabinet with pushing the front panel stopper.
- 3. Remove the front panel with pushing the both bottom lid stopper.
- 4. Remove 2 screws (B) and the connector (C) Connect P.C.B. can be removed.
- 5. Remove the cushion.
- 6. Remove the damper.
- 7. If remove the spring ① installed between the mechanism and the chassis, also mechanism can be removed.



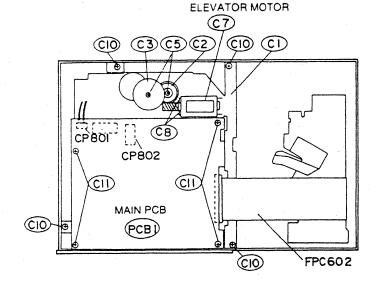
#### CD MECHANISM

- 1) How to remove the ELEVATOR MOTOR, FEED MOTOR and the LORDING MOTOR.
  - (1) How to remove the FEED MOTOR.
    - 1. Short the short-pattern of FPC, PU C99 with solder
    - 2. Remove the two screws (SCR S-TPG PAN PCS 2 ×3) C136 which fasten the MECHA-PANEL C134). The MECHA-PANEL can be removed.
    - 3. Remove the four screw (SCR S-TPG PAN PCS  $2 \times 4$ ) (C11) which fasten the MAIN PCB (PCB1).
    - 4. Remove the connectors (CP801 and CP802).
    - 5. Only when you will also remove the ELEVATOR MOTOR, remove FPC602.
    - Remove the five screws (SCR TPG PAN PCS 2×4) (C10) which fasten the TOP CHASSIS (C1).
       The TOP CHASSIS can be removed.
    - 7. Remove the two screw (SCR S-TPG PAN PCS 2 $\times$  2.5) C55 which fasten the FEED MOTOR C54 .

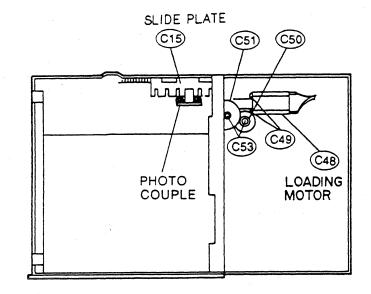


- (2) How to remove the ELEVATOR MOTOR.
  - 1. Remove the SPECIAL WASHER (C5) which fasten the GEAR A (C2) and the GEAR B (C3).

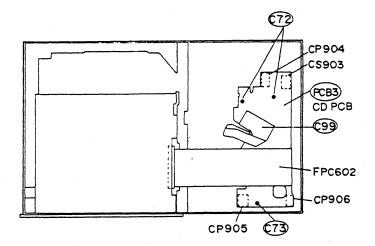
    The GEAR A and the GEAR B can be removed.
  - 2. Remove the two screws (SCR S-TPG PAN PCS  $2 \times 2.5$ ) (C8) which fasten the ELEVATOR MOTOR can be removed.



- (3) How to remove the LOADING MOTOR.
  - 1. Remove the SPECIAL WASHER C53 which fasten the GEAR LA C50 and GEAR LB C51. The GEAR LA and GEAR LB can be removed.
  - 2. Moving the slide plate C C15 to the right, lower the elevator to the third slot position.
  - 3. Remove the two screws (SCR S-TPG PAN PCS 2× 2.5) C49 which fasten the LOADING MOTOR C48 . The LOADING MOTOR can be removed.

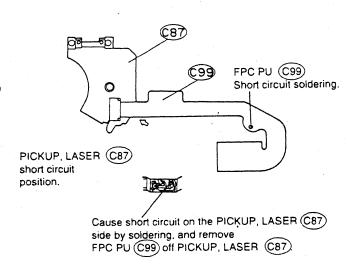


- 2) How to remove the PICKUP and the SPINDLE MOTOR.
  - (1) How to remove the CD PCB and PICKUP.
    - 1. Short the short-pattern of FPC, PU(C99) with solder.
    - 2. Remove CS903, FPC602, CP904, CP905 and CP906 from the connector.
    - 3. Remove the two screws (SCR S-TPG PAN PCS 2×4) C72 and one screws (SCR S-TPG BIN 2×7) C73) which fasten the CD PCB (PCB3).



- 4. Remove the screw (SCR S-TPG BIN  $2\times7$ ) C79 which fix INSIDE SWITCH C78.
- 5. Remove the two screws (SCR PAN PCS 1.7×3.5) (C98), and THRUST, SCREW (C96), PLATE, SLIDE (C97) will come off.
- 6. Remove the two screws SPECIAL SCREW C93 which fix SHAFT, PICKUP, A C89.
- 7. Remove the SHAFT, PICKUP, A C89 from the PICKUP, LASER C87.

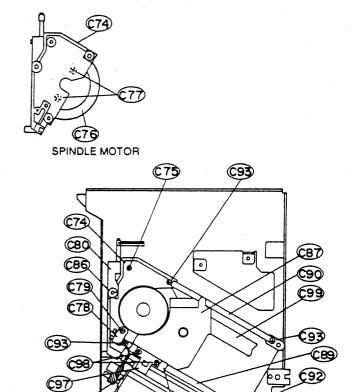
  The PICKUP, LASER C87 can be removed.
- 8. Cause short circuit by soldering in PICKUP, LASER (C87).
- 9. Remove FPC, PU (C99) off CONNECT, and CD P.C.B (PCB3) come off.



- (2) How to remove the SPINDLE MOTOR.
  - 1. Remove the screw (SCR S-TPG PAN PCS 2×4)

    (95) which fix MOUNT-M, SCREW (92) and SPRING, PLATE, SCREW (94).

    Remove the ASSY, SHAFT, SCREW (C88).
  - 2. Remove the two screws SPECIAL SCREW C93 which fix SHAFT, PICKUP, B C90.
  - 3. Remove the screw (SCR S-TPG PAN PCS  $2\times4$ ) (C75) which fix CHASSIS, SPINDLE (C74).
  - 4. Remove SPECIAL WASHER (286) which fix LE-VER, DISC (280).
  - 5. Remove the screw (SCR PAN PCS  $1.7 \times 2.2$ ) (C77), and SPINDLE MOTOR (C76) will come off.



**(296)** 

**(288)** 

**Ç94**)

C55

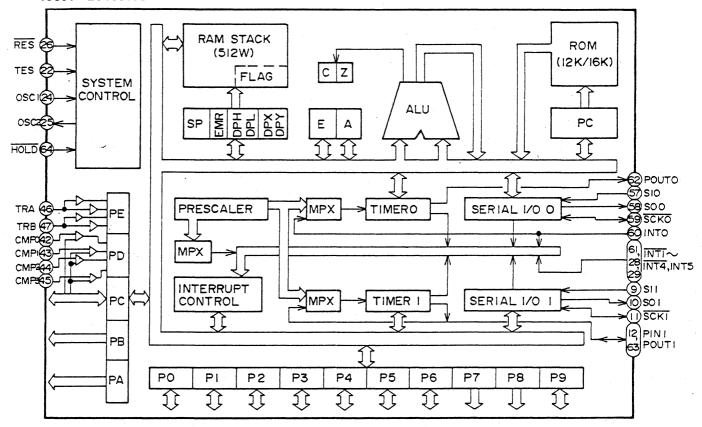
**FEED MOTOR** 

## IC601 - LC78620E

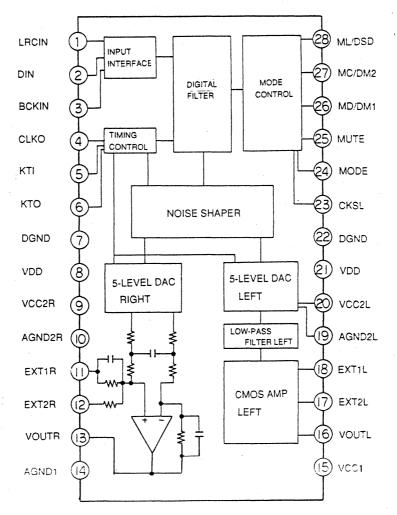
Terminal Number	Terminal Code	1/0		Fu	unc	tion Explanation	
1	DEFI	1	Defect detection signal (DEF) input terminal. (When unused, "L".)				
2	TAI	ı	For PLL Input terminal for testing. Pulldown resistance is self-contained			al for testing. Pulldown resistance is self-contained.	
3	PDO	0		Phase co	om	parison output terminal for outer VCO control.	
4	VVss			Power si	upp	bly terminal for self-contained VCO. Normally 0V.	
5	ISET	Al		Resistan	nce	connecting terminal for PDO output current adjustment.	
6	VVDD			L		minal for self-contained VCO. Normally 5V.	
7	FR	Al				ange frequency adjustment.	
8	Vss		Earthing terminal	<del></del>		tem. Normally 0V.	
9	EFMO	0	For slice level co			signal reverse output terminal.	
10	EFMO	0		ļ		signal output terminal.	
11	EFMIN					signal input terminal.	
12	TEST2	<u> </u>	Input terminal for			own resistance is self-contained.	
13	CLV+	<del>.</del>				vo control. Accelelates when CLV+ is "H", slows down	
14	CLV+		when CLV- "H".	pii.aio (		,	
15	V/P	0	Output terminal f			switchover monitor by rough servo / phase control. "H" control mode.	
16	FOCS	0	Output terminal f	or focus se	ervo	on/off. "L" causes focus servo ON.	
17	FST	0	Output terminal f	Output terminal for focus start pulse. Open drain output.			
18	FZD	1	Input terminal for	focus erro	or ze	ero cross signal. (When unused, "L".)	
19	HFL	1	Input terminal for	track dete	ectir	ng signal. Schmidt input.	
20	TES	l	Input terminal for	tracking e	rroi	signal. Schmidt input.	
21	PCK	0	Clock monitoring terminal for EFM data playback. At the time of phase lock, 4.3218MHz.				
22	FSEQ	0	Output terminal for synchronous signal detection. When synchronous signal detected from EFM signal and synchronous signal occurring inside correspond, "H".				
23	TOFF	0	Output terminal f	or tracking	OF	F.	
24	TGL	0	Output terminal f	or tracking	ga	in switchover. "L" raises gain.	
25	THLD	0	Output terminal f	or tracking	l ho	ld.	
26	TESTS	0	Output terminal f	or testing.	Pul	ldown resistance is self-contained.	
27	Vaa		Power supply ter	minal for d	ligit	al system. Normally 5V.	
28	JP+	0	direction jump, or	r slows dov	vn a	When JP+ is "H", accelerates at the time of outer track at the time of inner track direction jump. When JP- is "H", r track direction jump, or slows down at the time of outer	
29	JP-	0	track direction. 3	<u>value</u> outr	put	is possible by command.	
30	DEMO	1	Input terminal for Pulldown resista			n-on function at the time of set adjustment process. Itaind.	
31	TEST4	ı	1			own resistance is self-containd.	
32	EMPH	0	Output terminal f back.	or deemph	nasi	s monitor. At the time of "H", deemphasis disc is in play-	
33	LRCKO	0	Digital filter outpu	ut. W	Vor	d clock output.	
34	DFORO	0		R	СН	data output.	
35	DFOLO	0		L	СН	data output.	
36	DACKO	0		1		ock output.	
37	TESTIO	0	Output terminal f	or testing.	Op	en (Normally "L" output).	
38	USDACK	ı	Anti-shock corres	spondence	•	Bit clock input.	
39	USDFIN	1	input. (Unused, "	'L".)		LRch data.	

## IC CIRCUIT BLOCK DIAGRAM

### IC801 - LC66566B

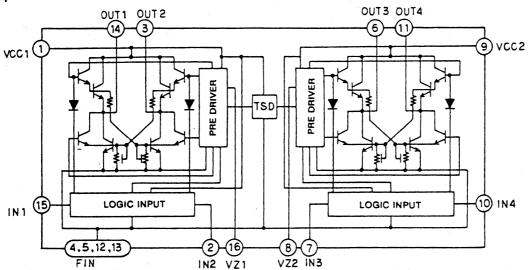


#### IC701 - PCM1710U

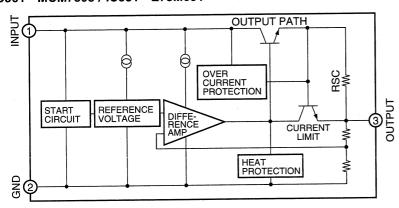


### IC CIRCUIT BLOCK DIAGRAM

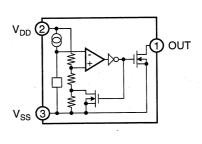
#### IC850 - LB1644



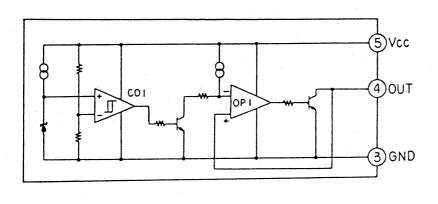
IC501 - MCM7805 / IC551 - L78M05T



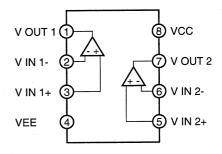
IC802 - S-8054HN

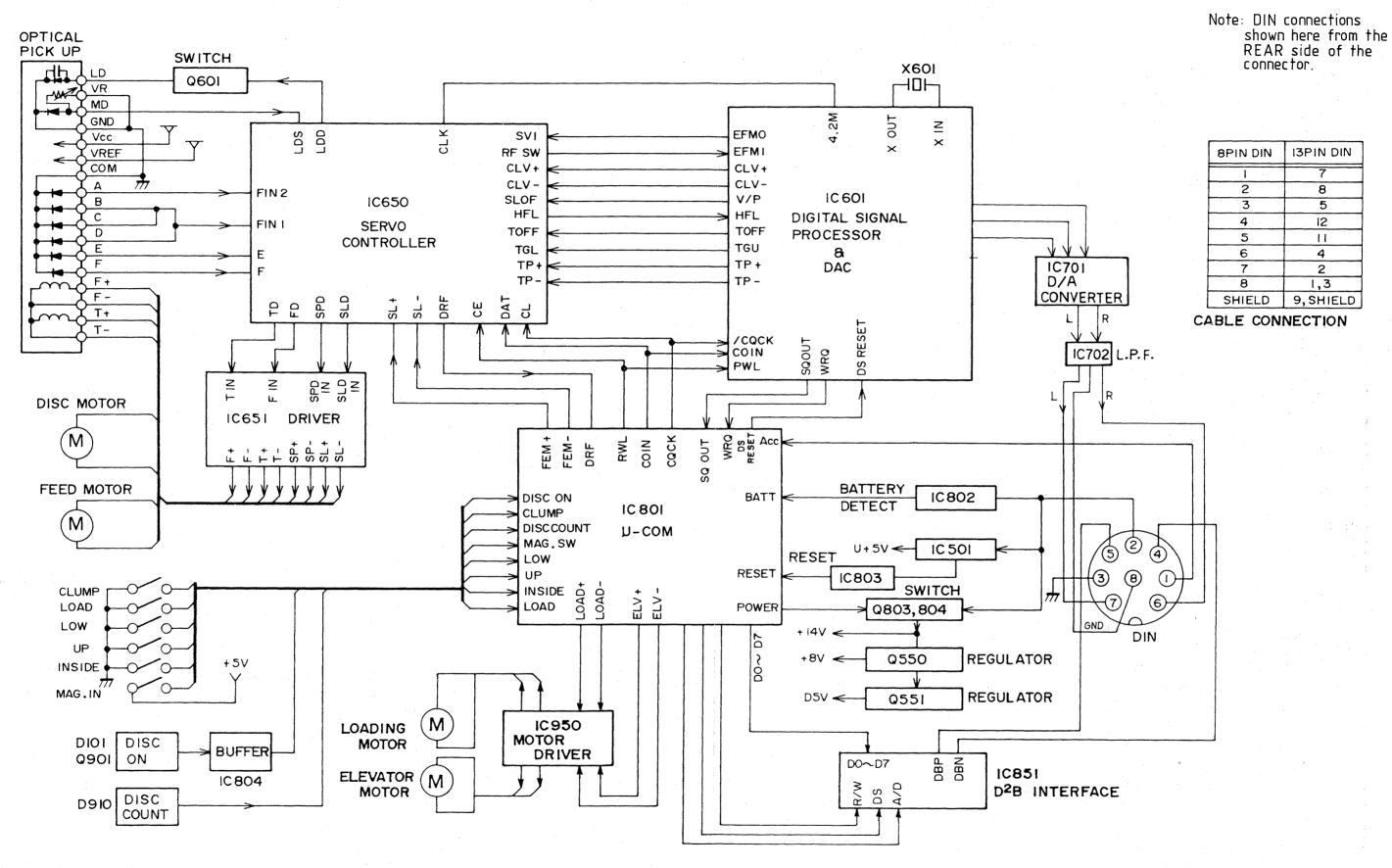


IC803 - PST9138N

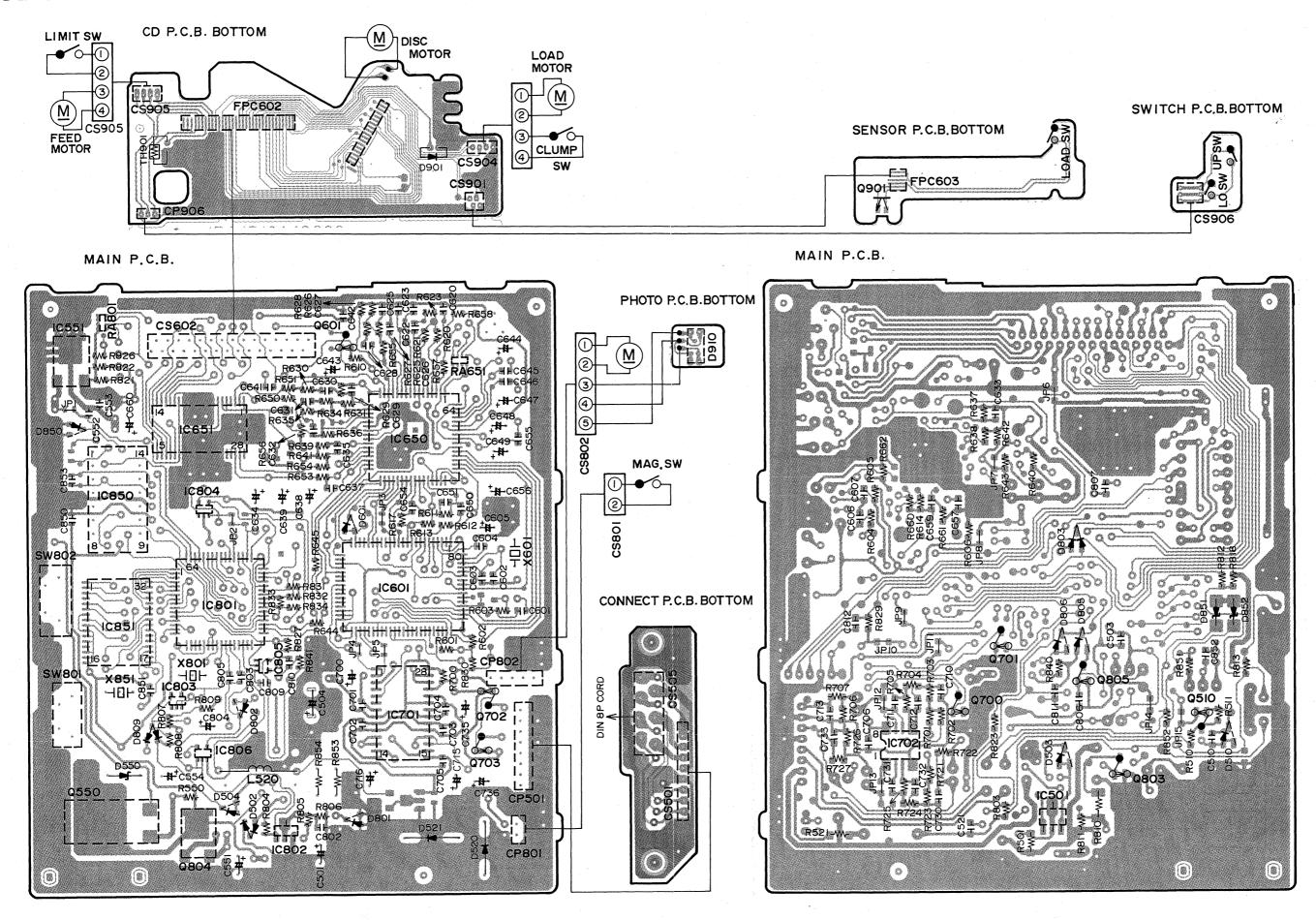


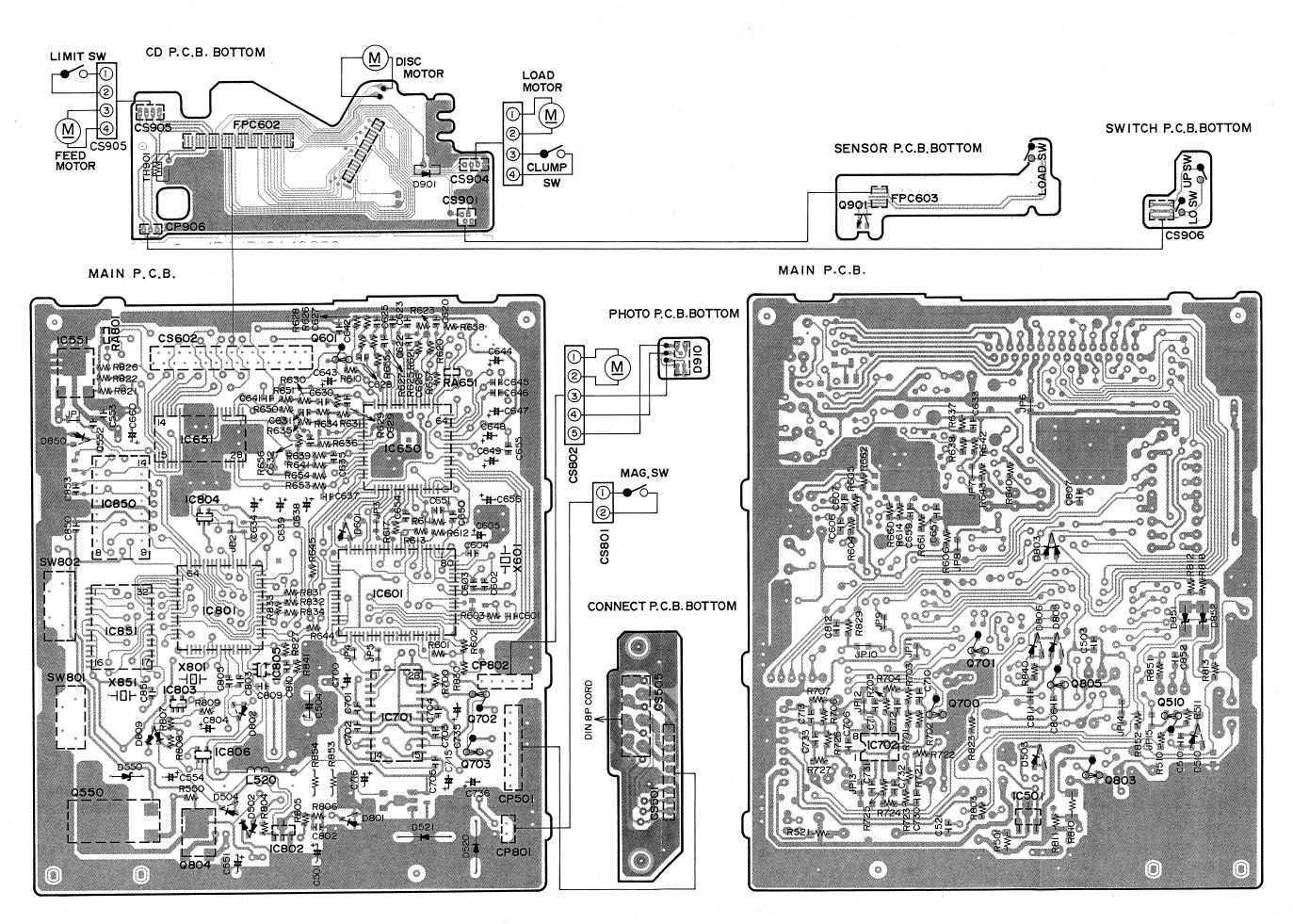
IC702 - NJM2100M

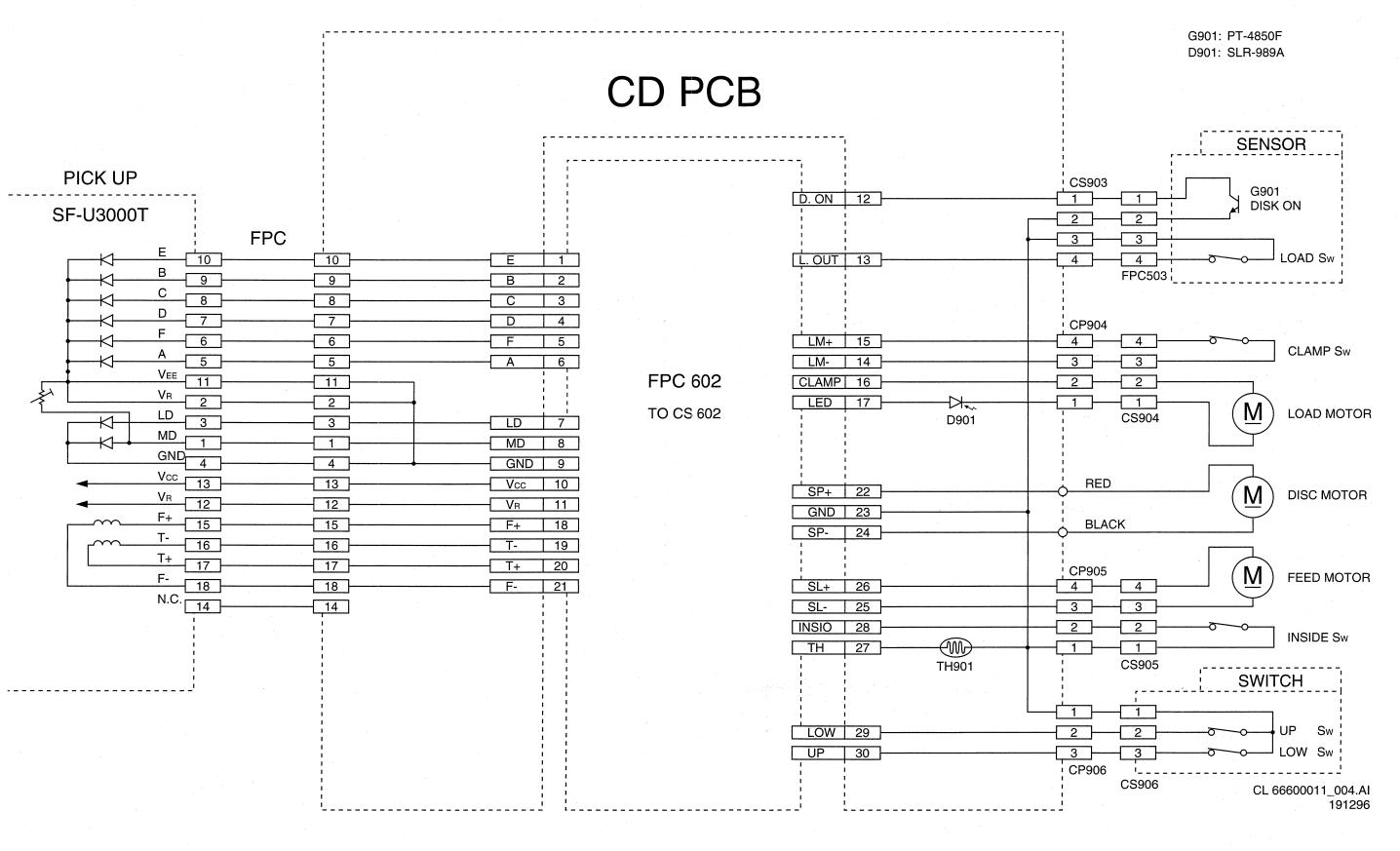




### PCB LAYOUT / CONNECTIONS

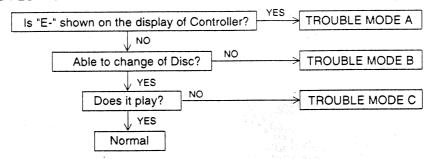






#### **TROUBLE SHOOTING**

### TROUBLE CHECKING FLOW CHART



E-30.....Temperature Trouble Message

reference temperature.

· The temperature inside the changer goes above its

#### 1. TROUBLE MODE A

"E" displayed by mechanical trouble of changer Mechanism. (At first, check voltage of car battery.)

## E-07..... TOC Reading Defective

- Disc is reversed
- · Disc is stained
- · Chucking is imperfect
- · Inner Circlelimit SW defective
- · Feed Motor Mechanism defective
- · P.U FPC Short has not removed

#### 2. TROUBLE MODE B

(Disc change is not completed within 30 seconds.)

- · Mechanism defective
- · Magazine defective
- · LOAD SW, UP SW, LOW SW, CLAMP SW, MAG SW defective.
- · Q901, D901, D910 defective

#### 3. TROUBLE MODE C

SYMPTOM	DEFECTIVE CIRCUIT	DEFECTIVE POINT
Disc Turning	Inferiority Feed Motor Circuit	Check inner circlelimit SW and CS602 28pin
		Check voltage of IC801
-		Check voltage of IC651 17, 18pin
		Check Feed Motor and Mechanism
	Focus Search Circuit	· Check voltage of IC650 16pin
		· Check voltage of IC651 11, 12pin
		· Check CS602 18, 21pin
		· Check pickup
	APC	· Check Q601 short or open
		- Check IC601 62,63pin
		Check pickup
	Disc Motor Circuit	- Check IC601 13pin and IC650 27pin
		· Check IC651 15, 16pin
		· Check Disc Motor and Mechanism
	Power Supply Circuit	- Check IC801 11,13pin
		· Check Q803, Q804
		Check Q550 and IC551
Track Search	Tracking Servo Circuit	· Check voltage of TP. TE and adjustment Inferiority of SVR651
		Check IC650 15pin and IC651 13,14pin
		Check IC651 3, 4pin
		· Check pickup
	Kick Pulse Circuit	· Check IC601 28, 29pin and IC650 14pin
	Feed Motor Circuit	Check IC650 29pin
		Check Feed Motor and Mechanism
Noise	RF Circuit	Check waveform of TP RF
	Mechanism	Check eccentricity of Mechanism and Disc rub
	Audio Circuit	Check IC701,702
,		Check Q700~703
		Check CP501 6, 7, 8pin

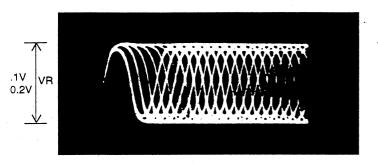
00 761

#### **TROUBLE SHOOTING**

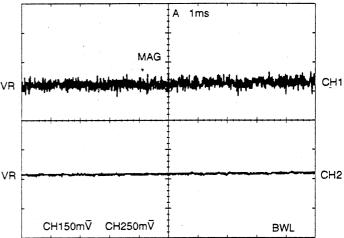
**WAVE FORM** 

Note: Reference voltage VR → TP, VR (2.50)

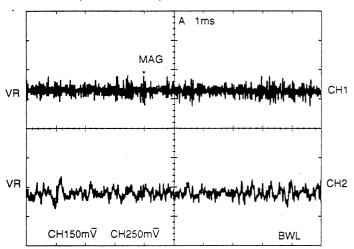
MODE PLAY RF (TP, RF) 0.5μs



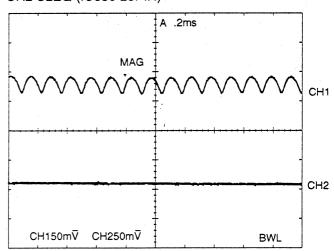
MODE PLAY CH1 TE (TP,TE) CH2 FE (TP,FE)



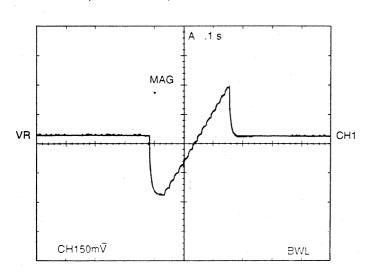
MODE PLAY CH1 TO (IC650 15PIN) CH2 FD (IC650 16PIN)



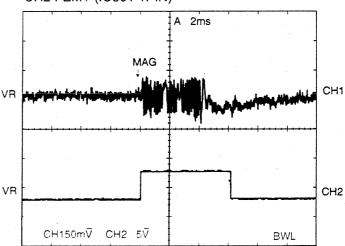
MODE PLAY CH1 SPD (IC650 27PIN) CH2 SLEQ (IC650 28PIN)



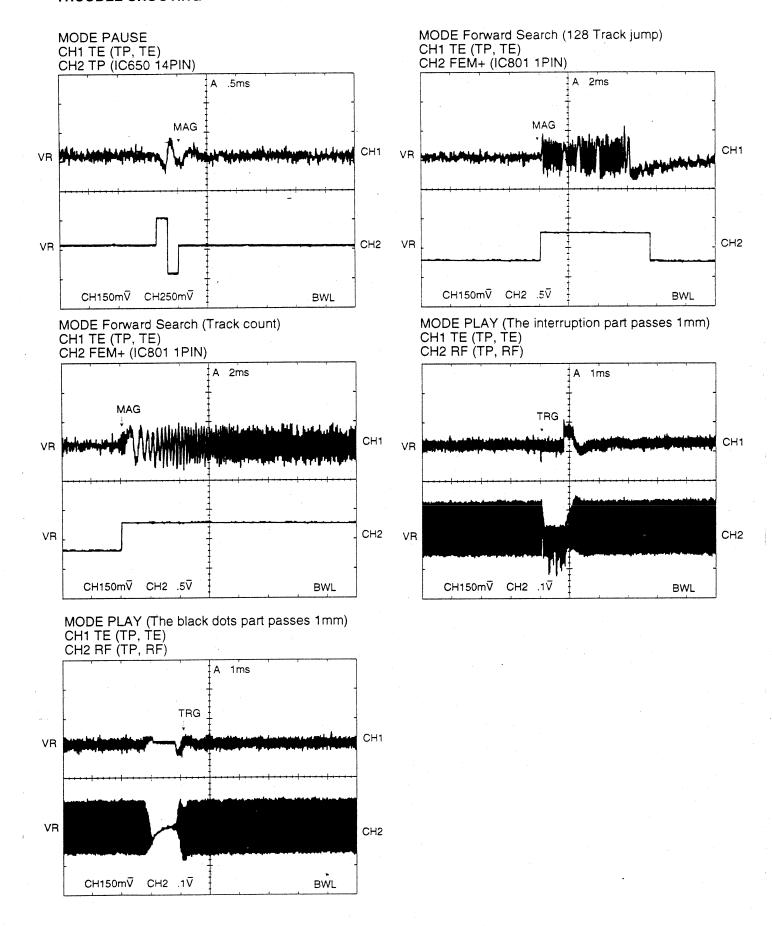
MODE FOCUS SEARCH CH1 FD (IC650 16PIN)



MODE Forward Search (64 Track jump) CH1 TE (TP, TE) CH2 FEM+ (IC801 1PIN)



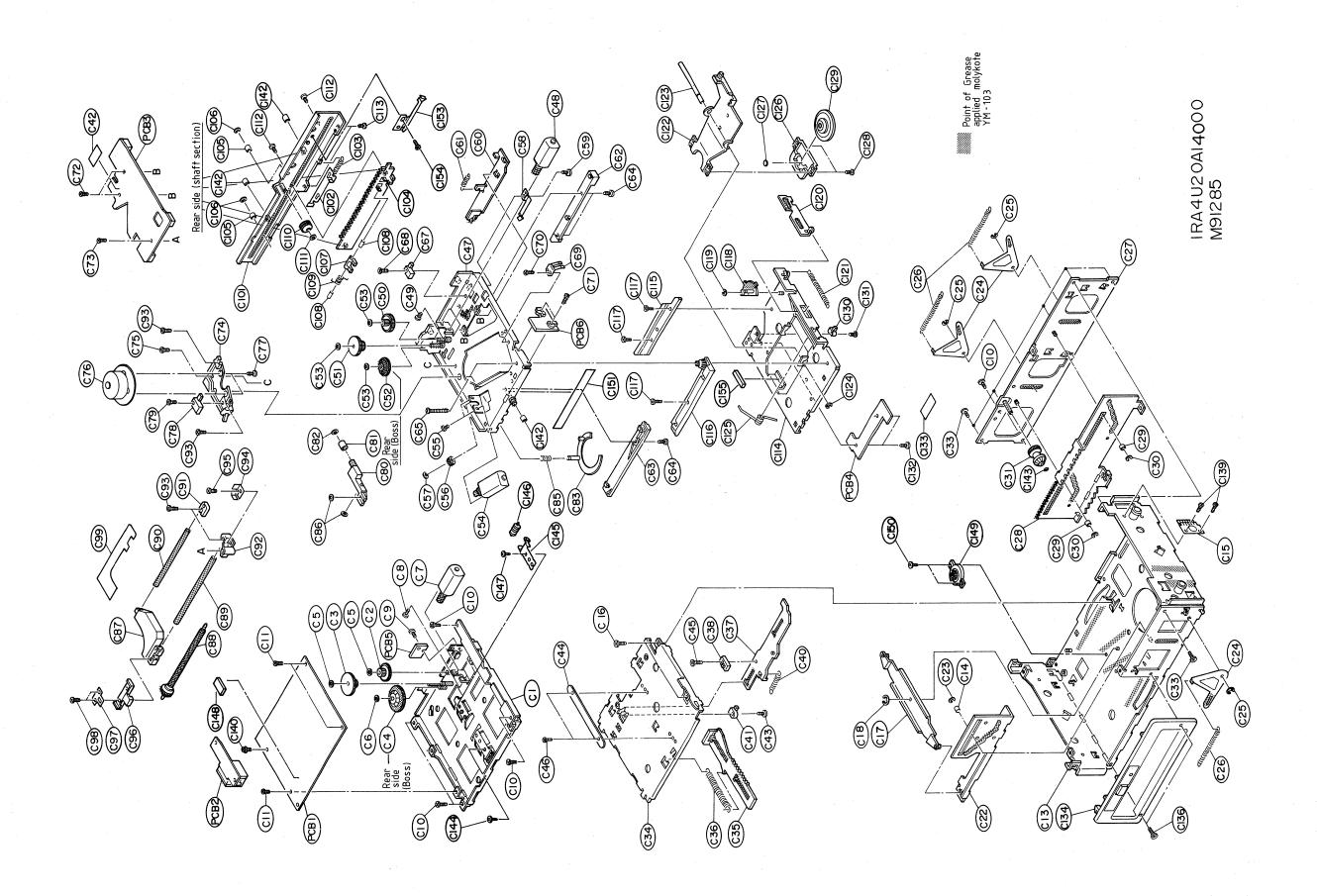
### **TROUBLE SHOOTING**



PCS 82 463

- 27 -

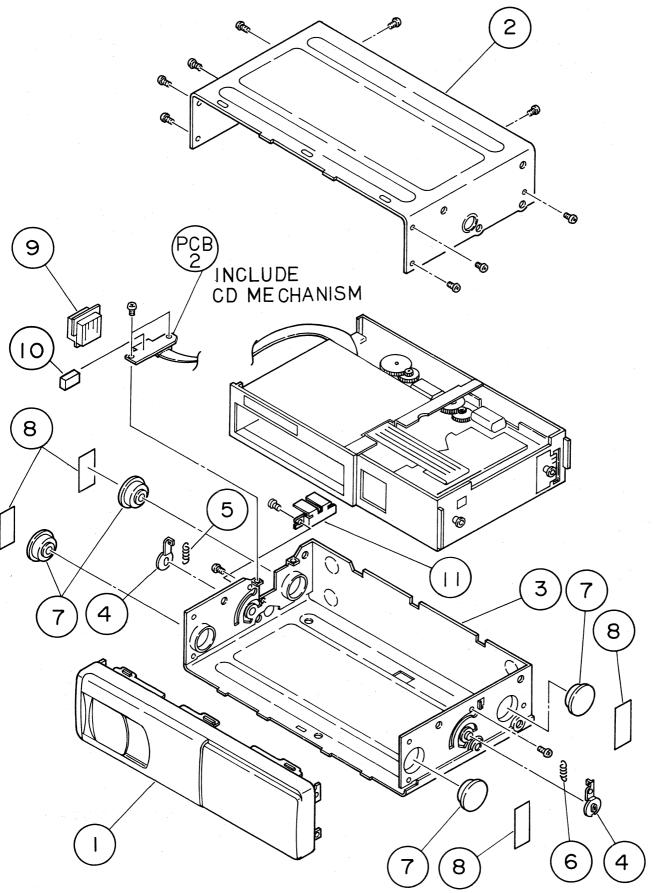
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## **MECHANICAL PARTS RC026**

C2	4822 522 33508	Gear, A
C3	4822 522 33509	Gear, B
C4	4822 522 33511	Gear, C
C5	4822 532 12294	Special washer
C6	4822 532 12295	Special washer
C7	4822 361 30441	Assy DC motor
C23	4822 532 12293	Special washer
C30	4822 532 12293	Special washer
C31	4822 522 33512	Gear, D
C41	4822 276 13547	Switch, push
C48	4822 361 30441	Assy DC motor
C50	4822 522 10595	Gear, LA
C51	4822 522 10596	Gear, LB
C52	4822 522 33514	Gear, G
C53	4822 532 12294	Special washer
C54	4822 361 10935	Assy CD feed motor
C56	4822 522 10597	Gear, FB
C57	4822 532 12735	Special washer
C67	4822 276 13547	Switch, push
C76	4822 361 10936	Assy DC motor
C78	4822 276 13547	Switch, push
C82	4822 532 12294	Special washer
C86	4822 532 12295	Special washer
C87	4822 691 10567	Pickup unit
C91	4822 535 10449	Shaft
C92 C96 C102 C104 C106	4822 502 14399 4822 463 11123 4822 402 61548 4822 402 61549 4822 532 12293	Screw Thrust, screw Lever, switch Assy lever/slide Special washer
C110	4822 522 33515	Gear, H
C111	4822 532 12294	Special washer
C129	4822 528 11071	Flange
C130	4822 276 13547	Switch, push
C134	4822 459 04421	Panel assy
C143 C145 C146	4822 532 12295 4822 402 10626 4822 528 11069 4822 321 62668 4822 320 11737	Special washer Arm, roller Roller Cable, 2p - 160mm Cable, 4p - 40mm
	4822 320 11738	Cable, 5p - 90mm

Note: all parts not mentioned here are no service parts!



IRA4U29A33000 RC026

## **CABINET PARTS AND ACCESSORIES**

•,		
Note: all pa	rts not mentioned here are no	service parts!
1	4822 459 04422	Front panel assy
2	4822 444 40787	Cabinet assy
3	4822 442 00686	Bottom lid assy
7	4822 529 10308	Damper
9	4822 462 10841	Cap for DIN connector
	4822 736 14834	Instructions for use
	4822 402 61551	Accessory part assy - brackets
	4822 310 10777	Accessory part assy - car fixation brackets
	4822 310 10778	Accessory part assy - brackets and bolts
	4822 321 62671	DIN extension cable 5.5m
	4822 321 62263	Rect.C - DIN interface cable
	4822 691 10569	CD magazine assy
	4822 502 21556	Transport screw

## **ELECTRICAL PARTS RC026**

Note: all parts not mentioned here are no service parts!

<b>→</b> I-			<del>6000000</del>		
D502	4822 130 82557	DCC010	IC501	4822 209 33241	MC147805AUT
D503	4822 130 10782	DSB015	IC551	4822 209 33094	L78M05T
D504	4822 130 83633	MA704A	IC601	4822 209 15225	LC78620E-D
D510	4822 130 83765	Zener 5.6V	IC650	4822 209 15227	LA9240M servo
D520	4822 130 31533	DS135	IC651	4822 209 15228	BA6999FP
D521	4822 130 31533	DS135	IC701	4822 209 15226	PCM1710U
D550	4822 130 10783	Zener 8.2V	IC702	4822 209 30455	NJM2100M
D601	4822 130 83631	DSB010	IC801	4822 209 15229	LC66566B-4H90
D801	4822 130 83632	Zener 5.1V	IC802	4822 209 63631	S-8054HN-CB
D802	4822 130 83631	DSB010	IC803	4822 209 15203	PST9138N
D803	4822 130 83637	DCB010	IC804	4822 209 32984	TC7SU04F
D805	4822 130 83637	DCB010	IC805	4822 209 32984	TC7SU04F
D806	4822 130 83631	DSB010	IC806	4822 209 72397	TC4S71F
D809	4822 130 10784	DCA010	IC850	4822 209 33758	LB1644
D850	4822 130 80273	Zener 8.2V	IC851	4822 209 32743	MSM6307GS
	· · · · · · · · · · · · · · · · · · ·				
D851	4822 130 10657	Zener 5.6V	MISCEL	LANEOUS	
D852	4822 130 10657	Zener 5.6V	CS505	4822 267 31758	DIN socket
D901	4822 130 10114	LED SLR-989A-AB	L520	4822 157 11124	SK 5mH
D910	4822 130 91369	Photo coupler SPI-235-1	SW801		Eject switch
	4822 130 63655	Photo diode PT4850F	SW802		Reset switch
			TH901	4822 111 92201	Thermistor
€			11,1001	4022 111 02201	(NTH4G42B104EB)
Q510	4822 130 60753	2SC2812-L6			(
Q550	4822 130 10786	2SD2199S-TSD-DR	X601	4822 242 81702	Crystal 16.9433MHz
Q601	4822 130 10787	2SA1338-6	X801	4822 242 73769	Ceramic resonator
Q700	4822 130 90323	DTC114TK			4.190MHz
Q701	4822 111 90813	UN2113	X851	4822 242 10678	Ceramic resonator
					5.75MHz
Q702	4822 130 90323	DTC114TK		4822 276 13547	UP switch
Q703	4822 130 90323	DTC114TK		4822 276 13547	LOW switch
Q803	4822 130 63551	DTC124XK			
Q804	4822 130 62912	2SB1202(ST)			
Q805	4822 130 10785	2SA1179-M6			
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## IC601 - LC78620E (CONT'D)

IC601 - LC	78620E (CO	NT'D)					
40	USDFIR	1	Anti-shock correspondence	Input terminal for testing. Normally "L".			
41	USLRCK	1	input. (unused, "L".)	Word clock output. (When unused, "L".)			
42	LRSY	0	ROMXA correspondence input	L/R clock output.			
43	CK2	0		Bit clock output. DACLK (At the time of RES) Polarity reverse (CK2CON mode)			
44	ROMAX	0		Data output. Data (Supplement) (At the time of RES) ROMOUT (ROMXA mode)			
45	C2F	0		C2 Frag output.			
46	MUTEL	0	For 1bit DAC	Mute output terminal.			
47	LVDD			Power supply terminal for L channel. Normally 5V.			
48	LCHP	0		L channel P output terminal.			
49	LCHN	0	1.	L channel N output terminal.			
50	LVss			Earthing terminal for L channel. Normally 0V.			
51	RVss			Earthing terminal for R channel. Normally 0V.			
52	RCHN	0		R channel N output terminal.			
53	RCHP	0		R channel P output terminal.			
54	RVDD	-		Earthing terminal for R channel. Normally 5V.			
55	MUTER	. 0		Mute output terminal.			
56	DOUT	0	Digital OUT output terminal.				
57	SBSY	0	Output terminal for synchronous	signal of sub-code block.			
58	EFLG	0	Terminal for monitoring C1, C2, s	ingle, double correction.			
59	PW	0	Output terminal for sub-code P, C				
60	SFSY	0	Output terminal for synchronous signal of sub-code frame. When sub-code is in standby, " = L ".				
61	SBCK	1	Input terminal for sub-code reado	ut clock. Schmidt input.			
62	FSX	0	Output terminal for 7.35KHz synchronous signal which is divided frequency from crystal oscillation.				
63	WRQ	0	Output terminal for sub-code Q or	utput standby.			
64	RWC	1	Input terminal for read/write contr	ol.			
65	SQOUT	0	Sub-code Q output terminal.				
66	COIN	1	Input terminal for command from	micro computer.			
67	CQCK	ı	Input terminal for command input Schmidt input.	intake clock, or sub- code offtake clock from SQOUT.			
. 68	RES.	<u> </u>	Chip reset input terminal. When p	power is supplied, changeover to "L" once.			
69	TST11	0	Input terminal for testing. Open (r	normally "L" output).			
70	LASER	0	Output terminal for laser ON/OFF puter.	F. Controls by serial data command from micro com-			
71	16M	0	16.9344MHz output terminal. But outputs 33.8688MHz, only in case of quadruple speed playback mode.				
72	4.2M	0	4.2336MHz output terminal.				
73	CONT	0	Spare output terminal. Controls by	y serial data command from micro computer.			
74	TEST5	1	Input terminal for testing. Pulldow	n resistance is self-contained.			
75	CS	1:	Chip select input terminal. Pulldov	wn resistance is self-contained.			
76	XVss		Earthing terminal for crystal oscilla	ation. Normally 0V.			
77	XIN	ı	Connecting terminal for 16.9344M	MHz crystal oscillator.			
78	Хот	0	Connects 33.8688MHz crystal os	cillator, in case of quadruple speed playback system.			
79	XVDD		Power supply terminal for crystal	oscillation. Normally 5V.			
80	TEST1	.	Input terminal for testing. Pulldow	n resistance is self-contained.			

IC CIRCUIT DESCRIPTION

## IC851 - MSM6307GS

No	Port Name	1/0	Description
1	D5	1/0	8bit bi-directional address or data bus
2	D6	1/0	8bit bi-directional address or data bus
3	D7	1/0	8bit bi-directional address or data bus
4	N, C		
5	R/W	-	Read/Write selector
6	A/D	1	Selects address or data on DO-D7
7	1 <sup>2</sup> C	1	Selects 1ºC or parallel inteface
8	DS	J	Data storobe to access data bus
9	INT	0	Interrupt output
10	AO .	1	Programmables 12C slave addresses
11	A1	1	Programmables 12C slave addresses
12	A2	ı	Programmables 12C slave addresses
13	N, C		
14	SBA	1/0	1ºC data signal input/output
15	SCL	1/0	l <sup>2</sup> C clock signal input/output
16	GND	_	GND

No	Port Name	1/0	Description
17	VDD		VDD (+5V)
18	N, C	_	
19	DBN	1/0	Differential D <sup>2</sup> B lines of the intemal driver/receiver
20	·DBP	1/0	Differential D <sup>2</sup> B lines of the intemal driver/receiver
21	TEST	1	Test mode of IC
22	BUS OUT	0	D <sup>2</sup> B output (TTL level)
23	BUS IN	I	D <sup>2</sup> B input (TTL level)
24	6СМО	0	Clock output SMHz resonator
25	6CM1	ı	Clock input 6MHz resonatore
26	POR	1	Power on reset
27	DO	1/0	8bit bi-directional address or data bus
28	N, C		
29	D1	1/0	8bit bi-directional address or data bus
30	D2	1/0	8bit bi-directional address or data bus
31	D3	1/0	8bit bi-directional address or data bus
32	D4	1/0	8bit bi-directional address or data bus

IC CIRCI IC851 - M

IC601 - L

EFMI

FSE CLV CLV SBS SFS COCI CDIF

No	Port Name	1/0	Description
1	D5	1/0	8bit bi-directional address or dat bus
2	D6	1/0	8bit bi-directional address or data bus
3	D7	1/0	8bit bi-directional address or data bus
4	N, C		
5	R/W	1	Read/Write selector
6	A/D	- 1	Selects address or data on DO-D7
7	1°C	1	Selects 1ºC or parallel inteface
8	DS	1	Data storobe to access data bus
9	INT	0	Interrupt output
10	AO	1	Programmables 1ºC slave addresses
11	A1	ı	Programmables 1ºC slave addresses
12	A2	1	Programmables 12C slave addresses
13	N, C		
14	SBA	1/0	l <sup>2</sup> C data signal input/output
15	SCL	1/0	l <sup>2</sup> C clock signal input/output
16	GND		GND

	No	Port Name	1/0	Description
	17	VDD		VDD (+5V)
$\left\{ \right.$	18	N, C		
	19	DBN	1/0	Differential D <sup>2</sup> B lines of the intemal driver/receiver
	20	·DBP	1/0	Differential D <sup>2</sup> B lines of the intemal driver/receiver
	21	TEST	I	Test mode of IC
	22	BUS OUT	0	D <sup>2</sup> B output (TTL level)
	23	BUS IN	1	D <sup>2</sup> B input (TTL level)
	24	6CM0	0	Clock output 6MHz resonator
	25	6CM1	I	Clock input 6MHz resonatore
	26	POR	1	Power on reset
	27	DO .	1/0	8bit bi-directional address or data bus
	28	N, C		
	29	D1	1/0	8bit bi-directional address or data bus
	30	D2	1/0	8bit bi-directional address or data bus
	31	D3	1/0	8bit bi-directional address or data bus
	32	D4	1/0	8bit bi-directional address or data bus

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Normally 0V.

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quency from crystal

clock from SQOUT.

nd from micro com-

case of quadruple

ocomputer.

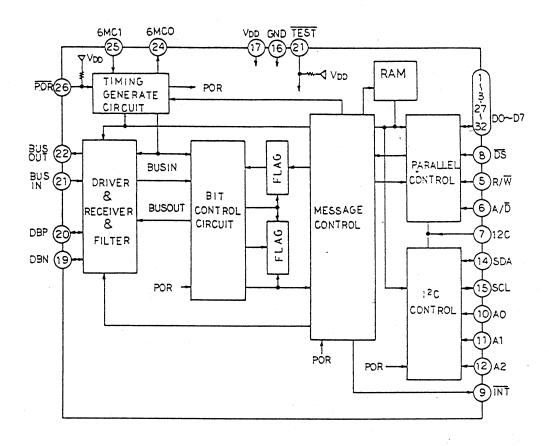
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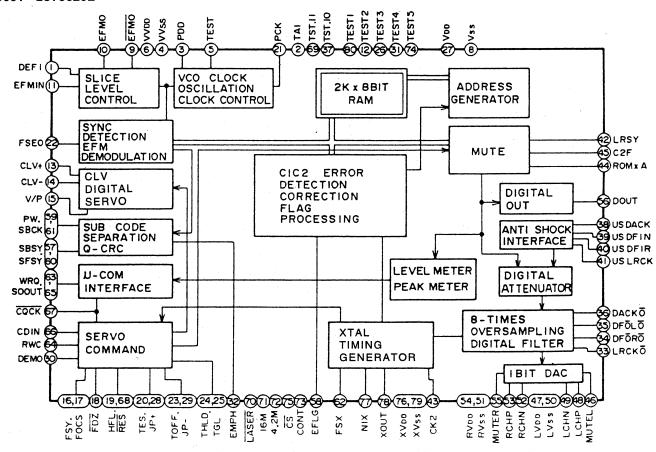
No	Port Name	1/0	Description
1	D5	1/0	8bit bi-directional address or data bus
2	D6	1/0	8bit bi-directional address or data bus
3	D7	1/0	8bit bi-directional address or data bus
4	N, C		
5	R∕W	1	Read/Write selector
6	A/D	١	Selects address or data on DO-D7
7	1 <sup>2</sup> C	1	Selects 1 <sup>2</sup> C or parallel inteface
8	DS	1	Data storobe to access data bus
9	INT	0	Interrupt output
10	AO .	1	Programmables 1ºC slave addresses
11	A1	1	Programmables 12C slave addresses
12	A2	ı	Programmables 1ºC slave addresses
13	N, C		
14	SBA	1/0	l <sup>2</sup> C data signal input/output
15	SCL	1/0	l <sup>2</sup> C clock signal input/output
16	GND		GND

## IC CIRCUIT BLOCK DIAGRAM

#### IC851 - MSM6307GS



#### IC601 - LC78620E



d playback system.

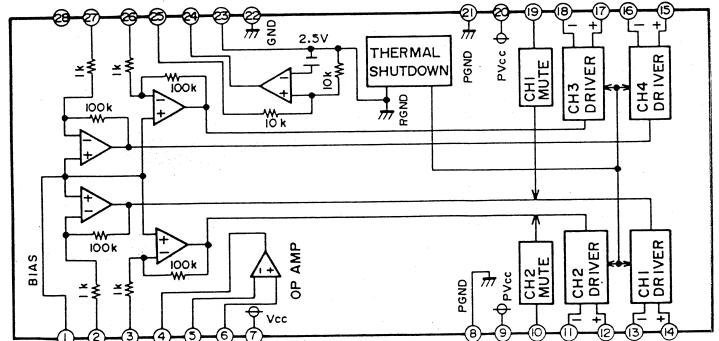
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tion.  Filkut photo-diode connecting pin. Pickup photo-diode connecting pin. Proms TE signal by subtraction from F pin. Pickup photo-diode connecting pin. Proms TE signal by subtraction from F pin. Pickup photo-diode connecting pin. Pin for inputting DC constituent of TE signal to TE pin.  TE TE signal output pin.  TESI TESI TESI TESI TESI TESI TESI TESI	IC650 - LA9240MS								
tion.  Final pickup photo-diode connecting pin. Forms TE signal by subtraction from F pin. Pickup photo-diode connecting pin. Forms TE signal by subtraction from F pin. Pickup photo-diode connecting pin.  TB TE Final protection gin.  Pin for inputting DC constituent of TE signal to TE pin.  TE TE signal output pin.  TE TE signal output pin.  TES (TRACK ERROR SENCE) comparator input pin. Band Pass TE signal, and input. Input pin for shock detection.  Constant setting pin at the time of tracking gain.  TA Pin for connecting high pass elimination condensor of servo.  Pin for constituting tracking phase compensation constant between TD and VR pin.  Pin for setting tracking pinase compensation.  Pin for setting tracking pinase compensation constant between FD and FA pin.  Pin for constituting focussing phase compensation constant between FA and FE-pin.  Pin for constituting focussing phase compensation constant between FA and FE-pin.  Pin for constituting focussing phase compensation constant between FA and FE-pin.  Pin for constituting focussing phase compensation constant between FA and FE-pin.  Pin for constituting focussing phase compensation constant between FA and FE-pin.  Pin for constituting focussing phase compensation constant between FA and FE-pin.  Pin for constituting focussing phase compensation constant between FA and FE-pin.  Pin for constituting focussing phase compensation constant between FA and FE-pin.  Pin for constituting focussing phase compensation constant between FA and FE-pin.  Pin for constituting focussing phase compensation constant between FA and FE-pin.  Pin for constituting focussing phase compensation constant between FA and FE-pin.  Pin for constituting focussing phase compensation constant between FA and FE-pin.  Pin for constituting fall gain as setting resistance at The time of spindle form forms forms.  Pin for consecuting pin for spindle phase compensation constant.  Sp.	PIN No.	PART NAME	EXPLANATION						
FIN1 Pickup photo-diode connecting pin.  FE pickup photo-diode connecting pin.  FE pickup photo-diode connecting pin.  FIN1 Pin for inputting DC constituent of TE signal by subtraction from F pin.  Pin for inputting DC constituent of TE signal to TE pin.  FESI TESI TESI TESI TESI TESI TESI TESI (TRACK ERROR SENCE) comparator input pin. Band Pass TE signal, and input.  Input pin for shock detection.  TH constant setting pin at the time of tracking gain.  Pin for connecting high pass elimination condensor of servo.  Pin for constituting pin at the time of tracking gain.  Pin for connecting high pass elimination condensor of servo.  Pin for setting tracking pina phase compensation constant between TD and VR pin.  Pin for setting tracking pina pignal (kick pulse) amplitude.  Tracking control signal output pin.  Pin for constituting tocusing phase compensation constant between FD and FA pin.  Pin for constituting tocusing phase compensation constant between FA and FE- pin.  Pin for constituting tocusing phase compensation constant between FA and FE- pin.  Pin for constituting tocusing phase compensation constant between FA and FE- pin.  Pin for constituting tocusing phase compensation constant between FA and FE- pin.  Pin for connecting FE signal gain setting resistance to TE pin.  AGND for analog signal.  SP Single end output for.  FE signal output pin.  FI sig	1	FIN2	Pickup photo-diode connecting pin. Forms RF signal by addition to FIN1 pin, and FE signal by subtraction.						
First protecting plants of the state of the	2	FIN1	Pickup photo-diode connecting pin.						
Fin for inputting DC constituent of TE signal.  Fin for connecting gain setting resistance of TE signal to TE pin.  TES (TRACK ERROR SENCE) comparator input pin. Band Pass TE signal, and input. Input pin for shock detection.  Constant setting pin at the time of tracking gain.  Pin for connecting high pass elimination condensor of servo.  Pin for connecting high pass elimination condensor of servo.  Pin for constituting tracking phase compensation constant between TD and VR pin.  Pin for setting tracking phase compensation.  Pin for constituting focussing phase compensation constant between FD and FA pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant together with SPD pin.  Spindle control signal output pin.  Connecting pin for Spindle phase compensation constant together with									
Fin for connecting gain setting resistence of TE signal of TE pin.  Fin for connecting function from the time of tracking gain.  Fin for constituting fracking phase compensation constant between TD and VR pin.  Fin for constituting fracking phase phase compensation.  Fin for constituting fracking phase compensation.  Fin for constituting fracking phase compensation.  Fin for constituting fracking phase compensation.  Fin for constituting focusing phase compensation constant between FD and FA pin.  Fin for constituting focusing phase compensation constant between FD and FA pin.  Fin for constituting focusing phase compensation constant between FA and FE-pin.  Fin for constituting focusing phase compensation constant between FA and FE-pin.  Fin for constituting focusing phase compensation constant between FA and FE-pin.  Fin for constituting focusing phase compensation constant between FA and FE-pin.  Fin for constituting focusing phase compensation constant between FA and FE-pin.  FE signal output pin.  Fin for connecting FE signal gain setting resistance to TE pin.  GND for analog signal.  SPI Single and output of CV+ and CV- pin input signal.  SPI Single amplifier input.  Connecting pin for gain setting resistance at the time of spindle 12cm mode.  Connecting pin for gain setting resistance at the time of spindle 12cm mode.  Connecting pin for set delivery signal from DSP.  SLE SLE Connecting pin for set delivery signal from DSP.  Input pin for set delivery signal from DSP.  Input pin for tracking gain control signal from DSP.  Input pin for tracking gain control signal from DSP. Off in case of TGL="H".  Input pin for tracking gain control signal from DSP.  Input pin for tracking gain control signal from DSP.  Input pin for tracking gain control signal from DSP.  Input pin for tracking gain control signal from DSP.  Input pin for tracking gain control signal from DSP.  Input pin for CLV error signal from DSP.  Input pin for control input pin.  Input pin for controlling data slice level by RF waveshape DSP.  In	4	F	Pickup photo-diode connecting pin.						
TE Signal output pin. TESI TES (SCI NCM ERROR SENCE) comparator input pin. Band Pass TE signal, and input. Input pin for shock detection. Constant setting pin at the time of tracking gain. Pin for connecting high pass elimination condensor of servo. Pin for connecting high pass elimination condensor of servo. Pin for constituting tracking phase compensation constant between TD and VR pin. Pin for setting tracking phase compensation constant between TD and VR pin. Pin for setting tracking phase compensation constant between TD and FA pin. Pin for constituting focusing phase compensation constant between FA and FE pin. Pin for constituting focusing phase compensation constant between FA and FE pin. Pin for constituting focusing phase compensation constant between FA and FE pin. Pin for constituting focusing phase compensation constant between FA and FE pin. Pin for constituting focusing phase compensation constant between FA and FE pin. Pin for constituting focusing phase compensation constant between FA and FE pin. Pin for constituting focusing phase compensation constant between FA and FE pin. Pin for constituting focusing phase compensation constant between FA and FE pin. Pin for connecting FE signal gain setting resistance to TE pin. GND for analog signal. SP Single end output of CV+ and CV- pin input signal. Spindle amplifier input. Connecting pin for spin setting resistance at the time of spindle 12cm mode. Connecting pin for Spindle phase compensation constant. Sled control signal output pin. Connecting pin for sled delivery signal from micro computer. Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from DSP. Pin put pin for tracking jump signal from DSP. Pin put pin for tracking jump signal from DSP. Pin put pin for tracking ginal control signal from DSP. Pin put pin for CLV error signal from DSP. Pin put pin for CLV error signal from DSP. Pin put pin for CLV error signal from DSP. Pin put pin for CLV error signal from DSP. Pin put pin for cutted final process pr			Pin for inputting DC constituent of TE signal.						
TESI SCI Input pin for shock detection. Constant setting pin at the time of tracking gain. Pin for connecting high pass elimination conordersor of servo. Pin for constituting tracking phase compensation constant between TD and VR pin. Pin for setting tracking jump signal (kick pulse) amplitude. To Pin for setting tracking jump signal (kick pulse) amplitude. Tracking control signal output pin. Pin for constituting tracking phase compensation constant between FD and FA pin. Pin for setting tracking jump signal (kick pulse) amplitude. Tracking control signal output pin. Pin for constituting focussing phase compensation constant between FA and FE pin. Pin for constituting focussing phase compensation constant between FA and FE pin. Pin for constituting focussing phase compensation constant between FA and FE pin. Pin for constituting focussing phase compensation constant between FA and FE pin. Pin for constituting focussing phase compensation constant between FA and FE pin. Pin for constituting focussing phase compensation constant between FA and FE pin. Pin for constituting focussing phase compensation constant between FA and FE pin. Pin for connecting FE signal gain setting resistance to TE pin. GND for analog signal. Single end output of CV+ and CV- pin input signal. SPG SPD Spindle control signal output pin. SPG SPD Spindle control signal output pin. Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from micro computer. Input pin for tracking jump signal from DSP. Gain low in case of TGL="H". Input pin for tracking jump signal from DSP. Pin put pin for tracking sin control signal from DSP. Pin put pin for control input pin. Pin for setting PR gain and EFM signal 3T compensation constant together with RFSM pin. SIL BASS SCI CELE LEVEL CONTROL) is output pin for controlling data slice level by RF wavesha									
9   SCI	1		TEC (TRACK ERROR SENCE) comparator input pin. Band Pass TE signal, and input						
TH Constant setting pin at the time of tracking gain.  TA Pin for connecting high pass elimination condensor of servo. Pin for constituting tracking phase compensation constant between TD and VR pin. Pin for setting tracking phase compensation. Pin for setting tracking jump signal (kick pulse) amplitude. Tracking control signal output pin. PD Pin for setting tracking phase compensation constant between FD and FA pin. PD Pin for constituting focussing phase compensation constant between FA and FE pin. Pin for constituting focussing phase compensation constant between FA and FE pin. Pin for constituting focussing phase compensation constant between FA and FE pin. Pin for constituting focussing phase compensation constant between FA and FE pin. PE signal output pin. Pin for constituting focussing phase compensation constant between FA and FE pin. PE signal output pin. Pin for connecting FE signal gain setting resistance to TE pin. GND for analog signal. Spindle amplifier input. Spindle amplifier input. Spindle amplifier input. Spindle amplifier input. Connecting pin for spindle phase compensation constant together with SPD pin. Spindle control signal output pin. Spindle control signal from micro computer. Input pin for sled delivery signal from DSP. JP- Input pin for sled delivery signal from DSP. JP- Input pin for tracking jump signal from DSP. Pin put pin for tracking jump signal from DSP. Pin put pin for tracking jump signal from DSP. Off in case of TGL="H". Input pin for tracking jump signal from DSP. Pin put pin for tracking off control signal from DSP. Pin put pin for other pin of the pin put pin. Pin for Setting PR gain and EFM signal 3T compensation constant together with RFSM pin. Pin for setting PR gain and EFM signal 3T compensation constant together with RFSM pin. Pin for Setting PR gain and EFM signal 3T compensation constant together with RFSM pin. Pin for contro									
TA Pin for connecting high pass elimination condensor of servo. Pin for constituting tracking phase compensation constant between TD and VR pin. Pin for setting tracking jump signal (kick pulse) amplitude. Tracking control signal output pin. Pin for constituting focussing phase compensation constant between FD and FA pin. Pin for constituting focussing phase compensation constant between FA- and FE- pin. Pin for constituting focussing phase compensation constant between FA- and FE- pin. Pin for constituting focussing phase compensation constant between FA- and FE- pin. Pin for constituting focussing phase compensation constant between FA- and FE- pin. Pin for constituting focussing phase compensation constant between FA- and FE pin. FE signal output pin. Pin for connecting FE signal gain setting resistance to TE pin. GND for analog signal. SP Single end output of CV+ and CV- pin input signal. SP Spindle amplifier input. Connecting pin for spindle phase compensation constant together with SPD pin. Spindle amplifier input. Connecting pin for Spindle phase compensation constant. SLEQ Connecting pin for seld phase compensation constant. SLEQ SLEQ Connecting pin for seld phase compensation constant. SLEQ Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking gain control signal from DSP. Gain low in case of TGL="H". Input pin for tracking gain control signal from DSP. Gain low in case of TGF="H". Input pin for tracking gain control signal from DSP. RF output pin for CLV error signal from DSP. RF output pin for CLV error signal from DSP. RF output pin for CLV error signal from DSP. RF output pin for CLV error signal from DSP. RF output pin for CLV error signal from DSP. RF Output pin for CLV error signal from DSP. RF Output pin for CLV error signal from DSP. RF Output pin for CLV error signal from DSP. RF Output pin for controlling data slice level by RF wavesh	1 1								
TD. Pin for constituting tracking phase compensation constant between TD and VR pin.  Pin for setting tracking phase compensation.  Pin for setting tracking phase compensation.  Pin for setting tracking jump signal (kick pulse) amplitude.  Tracking control signal output pin.  Procusing control signal output pin.  Procusing control signal output pin.  Pin for constituting focussing phase compensation constant between FD and FA pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for constituting focussing phase compensation constant between FA and FE pin.  Pin for connecting FE signal gain setting resistance to TE pin.  SP SP Single end output of CV+ and CV- pin input signal.  Spindle amplifier input.  Spindle amplifier input.  Connecting pin for spindle phase compensation constant together with SPD pin.  Spindle control signal output pin.  SLEQ Connecting pin for Sied phase compensation constant.  Sled control signal output pin.  SL Input pin for sled delivery signal from micro computer.  Input pin for sled delivery signal from DSP.  Input pin for tracking jump signal from DSP.  Input pin for tracking jump signal from DSP.  Pinput pin for tracking gain control signal from DSP. Gain low in case of TGL="H".  Input pin for tracking gain control signal from DSP.  Pinput pin for tracking gain control signal from DSP.  Input pin for tracking gain control signal from DSP.  Input pin for CLV error signal from DSP.  Refull FREGENCY LEVEL) is used to judge whether main beam is located above pit or above m  Sled serve off control input pin.  Pin for setting Regain and EFM signal 3T compensation constant together with RFSM pin.  SLO GND pin of digital system.  For Fore Sos Smoosing capacita output pin.  Tracking Balance control pin.  NC NO	1 1		Pin for connecting high pass elimination condensor of servo.						
13			Pin for constituting tracking phase compensation constant between TD and VR pin.						
14	1 1		Pin for setting tracking phase compensation.						
FD	14	JP							
FD. Pin for constituting focussing phase compensation constant between FD and FA pin. Pin for constituting focussing phase compensation constant between FA and FE pin. Pin for constituting focussing phase compensation constant between FA and FE pin. Pin for constituting focussing phase compensation constant between FA and FE pin. FE signal output pin. Pin for connecting FE signal gain setting resistance to TE pin. GND for analog signal. Spl Single end output of CV+ and CV- pin input signal. Spindle amplifier input. Connecting pin for gain setting resistance at the time of spindle 12cm mode. Connecting pin for Spindle phase compensation constant together with SPD pin. Spindle control signal output pin. Connecting pin for sled phase compensation constant. Sled control signal output pin. Sled control signal output pin. Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking gain control signal from DSP. Gain low in case of TGL="H". Output pin of TES signal to DSP. HFL (HIGH FREOENCY LEVEL) is used to judge whether main beam is located above pit or above m Sled servo off control input pin. Sled servo off control input pin. Sl. Sl. Sl. Input pin for cLV error signal from DSP. RF output pin for control linput pin. Sl. Sl. GND pin for setting RF gain and EFM signal 3T compensation constant together with RFSM pin. Sl. GND pin of digital system. Pin for setting RF gain and EFM signal 3T compensation constant together with RFSM pin. GND pin of digital system. Pin for controlling data slice level by DSP. GND pin of digital system. NC OONNECT. Output pin for controlling data slice level by DSP. Output pin for controlling data slice level by DSP. Output pin for controlling data slice level by DSP. Output pin for controlling data slice level by DSP. Output pin for controlling data slice level by DSP. Output pin for controlling data slice level by DSP. Output pin for det	15								
FA 19 FA 19 FA 19 FA 19 FA 10 FF 10 FE 20 FE 21 FE 21 FE 22 AGND 23 SP 24 SPI 25 SPG 26 Connecting pin for gain setting resistance to TE pin. 27 SPD 28 SLEQ 29 SLD 30 SL 31 SL 32 SP 33 SP 34 SPI 35 SIngle end output of CV+ and CV- pin input signal. 36 SP 37 SPD 38 SLEQ 39 SLD 30 SL 31 SL+ 32 JP- 33 JP+ 34 TGL 35 TOFF 36 TES 37 HFL 38 SLOF 38 SLOF 39 SLOF 30 SLOF 3			Focusing control signal output pin.						
Pin for constituting focussing phase compensation constant between FA and FE pin. FE signal output pin. FE signal output pin. GND for analog signal. SP Single end output of CV+ and CV- pin input signal. SPI Spindle amplifier input. Connecting pin for gain setting resistance at the time of spindle 12cm mode. Connecting pin for Spindle phase compensation constant together with SPD pin. SPD Spindle control signal output pin. SLEQ Connecting pin for sled phase compensation constant. SID Sled control signal output pin. SL+ Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from DSP. Input pin for tracking jump signal from DSP. JP- Input pin for tracking jump signal from DSP. Input pin for tracking gain control signal or DSP. Input pin for tracking gain control signal from DSP. Gain low in case of TGL="H". Input pin for tracking off control signal from DSP. Off in case of TOFF="H". CV+ Input pin for tracking to DSP. Input pin for tracking upps signal from DSP. Input pin for tracking pin to DSP. Input pin for tracking upps signal from DSP. Input pin for tracking upps signal from DSP. Sled servo off control input pin. From setting RF gain and EFM signal 3T compensation constant together with RFSM pin. (SLICE LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP. Input pin for controlling data slice level by DSP. GND pin of digital system. For Focs Smoosing capacita output pin. Tracking Balance control pin. NO CONNECT. Output pin for detecting disc defect.			Pin for constituting focusing phase compensation constant between FD and FA pin.						
FE Signal output pin. Pin for connecting FE signal gain setting resistance to TE pin. GNDD for analog signal. SP Single end output of CV+ and CV- pin input signal. Spindle amplifier input. Spindle amplifier input. Connecting pin for gain setting resistance at the time of spindle 12cm mode. Connecting pin for Spindle phase compensation constant together with SPD pin. Spindle control signal output pin. Spindle control signal output pin. Sled control signal output pin. Sled control signal output pin. Sled control signal output pin. Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking gain control signal from DSP. Gain low in case of TGL="H". TOFF Input pin for tracking one ontrol signal from DSP. Off in case of TOFF="H". Output pin of TES signal to DSP. HFL SLOF Sled servo off control input pin. CV- Input pin for CLV error signal from DSP. Input pin for tracking off control signal from DSP. Input pin for CLV error signal from DSP. Input pin for CLV error signal from DSP. SLOF Sled servo off control input pin. RFSM RF output pin. SLC (SLICE LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP. Input pin for controlling data slice level by DSP. GND pin of digital system. For Focs Smoosing capacita output pin. Tracking Balance control pin. NC ONONECT. Output pin for detecting disc defect.	1		Pin for constituting focussing phase compensation constant between FA and FE pin.						
Pin for connecting FE signal gain setting resistance to TE pin.  AGND SP SP Single end output of CV+ and CV- pin input signal.  SPI Spindle amplifier input. Connecting pin for gain setting resistance at the time of spindle 12cm mode. Connecting pin for Spindle phase compensation constant together with SPD pin. SPD Spindle control signal output pin. Connecting pin for sled phase compensation constant. Sled control signal output pin. Sl.+ Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from micro computer. Input pin for tracking jump signal from DSP. Off in case of TGL="H". Input pin for tracking off control signal from DSP. Off in case of TOFF="H". Output pin of TES signal to DSP. HFL (HIGH FREQENCY LEVEL) is used to judge whether main beam is located above pit or above m Sled servo off control input pin. Input pin for CLV error signal from DSP. Input pin for CLV error signal from DSP. FSSM RF output pin. Pin for setting RF gain and EFM signal 3T compensation constant together with RFSM pin. SLC SLICE LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP. Input pin for controlling data slice level by DSP. GND pin of digital system. For Focs Smoosing capacita output pin.									
AGND SP SP Single end output of CV+ and CV- pin input signal. Single end output of CV+ and CV- pin input signal. SPG SPG Connecting pin for gain setting resistance at the time of spindle 12cm mode. Connecting pin for Spindle phase compensation constant together with SPD pin. SPD Spindle control signal output pin. SLEQ Connecting pin for sled phase compensation constant. Sled control signal output pin. SLL Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from micro computer. Input pin for tracking jump signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking gain control signal from DSP. Gain low in case of TGL="H". Input pin for tracking off control signal from DSP. Off in case of TOFF="H". Output pin for TES signal to DSP. (HIGH FREQENCY LEVEL) is used to judge whether main beam is located above pit or above m Sled servo off control input pin. Input pin for CLV error signal from DSP. Input pin for CLV error signal from DSP. Input pin for CLV error signal from DSP. SLC SLC ELEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP. Input pin for controlling data slice level by DSP. GND pin of digital system. For Focs Smoosing capacita output pin. For Focs Smoosing capacita output pin. Tracking Balance control pin. NC ON CONNECT. Output pin for detecting disc defect.			Pin for connecting FE signal gain setting resistance to TE pin.						
SP Single end output of CV+ and CV- pin input signal. SPI Spindle amplifier input. Connecting pin for gain setting resistance at the time of spindle 12cm mode. Connecting pin for Spindle phase compensation constant together with SPD pin. SPD Spindle control signal output pin. Connecting pin for sled phase compensation constant. SLEQ SLD Sled control signal output pin. Connecting pin for sled phase compensation constant. SLD Sled control signal output pin. Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from DSP. JP- Input pin for tracking jump signal from DSP. JP- Input pin for tracking jump signal from DSP. JP- Input pin for tracking gain control signal from DSP. Gain low in case of TGL="H". Input pin for tracking off control signal from DSP. Off in case of TOFF="H". Output pin of TES signal to DSP. HFL (HIGH FREQENCY LEVEL) is used to judge whether main beam is located above pit or above m Sled servo off control input pin. Input pin for CLV error signal from DSP. Input pin for CLV error signal from DSP. RFSM RF output pin. Pin for setting RF gain and EFM signal 3T compensation constant together with RFSM pin. (SLICE LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP. Input pin for controlling data slice level by DSP. GND pin of digital system. For Focs Smoosing capacita output pin. Tracking Balance control pin. NO CONNECT. Output pin for detecting disc defect.									
SPI Spindle amplifier input. Connecting pin for gain setting resistance at the time of spindle 12cm mode. SP- Connecting pin for Spindle phase compensation constant together with SPD pin. Spindle control signal output pin. SLEQ SLD Spindle control signal output pin. SLEQ SLD Input pin for sled phase compensation constant. Sled control signal output pin. Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from DSP. Input pin for sled delivery signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking gain control signal from DSP. Off in case of TGL="H". Input pin for tracking gef control signal from DSP. Off in case of TOFF="H". Output pin of TES signal to DSP. IFES Output pin of TES signal to DSP. INPUT pin for tracking in DSP. INPUT pin for tracking in DSP. INPUT pin for tracking gef control signal from DSP. Off in case of TOFF="H". INPUT pin for tracking gef control signal from DSP. INPUT pin for tracking gef control signal from DSP. INPUT pin for tracking gef control signal from DSP. INPUT pin for tracking gef control signal from DSP. INPUT pin for tracking gef control signal from DSP. INPUT pin for tracking gef control signal from DSP. INPUT pin for CLV error signal from DSP. INPUT pin for controlling data slice level by RF waveshape DSP. INPUT pin for controlling data slice level by DSP. INPUT pin for controlling data slice level by DSP. INPUT pin for detecting disc defect.  INPUT pin for detecting disc defect.									
SPG SPG SPD Connecting pin for gain setting resistance at the time of spindle 12cm mode. Connecting pin for Spindle phase compensation constant together with SPD pin. Spindle control signal output pin. SLEQ SLD SLD SLD Sled control signal output pin. Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from micro computer. Input pin for tracking jump signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking gain control signal from DSP. Gain low in case of TGL="H". Input pin for tracking off control signal from DSP. Off in case of TOFF="H". Output pin of TES signal to DSP. HFL SLOF SIGNED SLOF SIGNED SLOF SIGNED SLOF SIGNED SLOF SIGNED SLOF SIGNED SLOF SLOF SLOF SLOF SLOF SLOF SLOF SLOF			Spindle amplifier input.						
SPD Spindle control signal output pin. Connecting pin for sled phase compensation constant. SLC Sled control signal output pin. Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from DSP. JP- Input pin for tracking jump signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking gain control signal from DSP. Gain low in case of TGL="H". Input pin for tracking off control signal from DSP. Off in case of TOFF="H". Output pin of TES signal to DSP. Input pin for tracking off control signal from DSP. Off in case of TOFF="H". SLOF Sled servo off control input pin. SLOF Sled servo off control input pin. Input pin for CLV error signal from DSP. Input pin for CLV error signal from DSP. Input pin for CLV error signal from DSP. RFSM RFSM RF output pin. Pin for setting RF gain and EFM signal 3T compensation constant together with RFSM pin. SLC (SLICE LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP. Input pin for controlling data slice level by DSP. GND pin of digital system. For Focs Smoosing capacita output pin. Tracking Balance control pin. NC NO CONNECT. Output pin for detecting disc defect.		SPG	Connecting pin for gain setting resistance at the time of spindle 12cm mode.						
SLEQ SLD									
SLD SL- SL- SL- Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from micro computer. Input pin for sled delivery signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking jump signal from DSP. Input pin for tracking gain control signal from DSP. Gain low in case of TGL="H". Input pin for tracking off control signal from DSP. Off in case of TOFF="H".  TOFF Input pin for tracking off control signal from DSP. Off in case of TOFF="H".  Unput pin for tracking off control signal from DSP. Input pin for tracking off control signal from DSP. Input pin for tracking off control signal from DSP. Input pin for CEVEL) is used to judge whether main beam is located above pit or above m  SLOF Sled servo off control input pin. Input pin for CLV error signal from DSP. Input pin for CLV error signal from DSP. Input pin for CLV error signal from DSP. RFS- RF output pin. Pin for setting RF gain and EFM signal 3T compensation constant together with RFSM pin.  (SLICE LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP. Input pin for controlling data slice level by DSP. GND in of digital system. For Focs Smoosing capacita output pin. Tracking Balance control pin. NO CONNECT. Output pin for detecting disc defect.									
Input pin for sled delivery signal from micro computer.									
SL+   Input pin for sled delivery signal from micro computer.   Input pin for tracking jump signal from DSP.   Input pin for tracking jump signal from DSP.   Input pin for tracking gain control signal from DSP.   Gain low in case of TGL="H".   Input pin for tracking gain control signal from DSP.   Off in case of TOFF="H".   Output pin of TES signal to DSP.   Output pin of TES signal to DSP.   Off in case of TOFF="H".   Output pin of TES signal to DSP.   Output pin of TES signal to DSP.   Output pin of TES signal from DSP.   Output pin for CLV error signal from DSP.   Input pin for CLV error signal from DSP.   RFSM   RFSM   RF output pin.   Output pin for controlling Alam Signal 3T compensation constant together with RFSM pin.   Output pin for controlling data slice level by RF waveshape DSP.   Input pin for controlling data slice level by RF waveshape DSP.   Output pin for digital system.   FSC   For Focs Smoosing capacita output pin.   Tracking Balance control pin.   NO CONNECT.   Output pin for detecting disc defect.   Output pin for detecting disc defec			Input pin for sled delivery signal from micro computer						
JP- JP+	i i		Input pin for sled delivery signal from micro computer.						
JP+ TGL TOFF Input pin for tracking jump signal from DSP. Gain low in case of TGL="H". Input pin for tracking gain control signal from DSP. Gain low in case of TGL="H". Input pin for tracking off control signal from DSP. Off in case of TOFF="H". Output pin of TES signal to DSP. HFL SLOF Sled servo off control input pin. Input pin for CLV error signal from DSP. Input pin for CLV error signal from DSP. Input pin for CLV error signal from DSP. Froutput pin. Pin for setting RF gain and EFM signal 3T compensation constant together with RFSM pin. SLC SLIC (SLICE LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP. Input pin for controlling data slice level by DSP. GND pin of digital system. For Focs Smoosing capacita output pin. Tracking Balance control pin. NO CONNECT. Output pin for detecting disc defect.									
TGL TOFF TOFF TES TES TES TES TOFF TES			Input pin for tracking jump signal from DSP.						
TES  Output pin of TES signal to DSP.  (HIGH FREQENCY LEVEL) is used to judge whether main beam is located above pit or above m  Sled servo off control input pin.  Sled servo off control input pin.  Input pin for CLV error signal from DSP.  Input pin for CLV error signal from DSP.  RFSM  RFSM  RFS-  Pin for setting RF gain and EFM signal 3T compensation constant together with RFSM pin.  SLC  (SLICE LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP.  Input pin for controlling data slice level by DSP.  GND pin of digital system.  For Focs Smoosing capacita output pin.  Tracking Balance control pin.  NO CONNECT.  Output pin for detecting disc defect.		1	Input pin for tracking gain control signal from DSP. Gain low in case of TGL="H".						
HFL SLOF Sled servo off control input pin. Input pin for CLV error signal from DSP. Input pin for CLV error signal from DSP. Input pin for CLV error signal from DSP. RFSM RFS- SLC SLC SLIC LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP. Input pin for controlling data slice level by DSP. GND pin of digital system. For Focs Smoosing capacita output pin. Tracking Balance control pin. NC OUTPUT DIA NC OUTPUT NC OUTPUT DIA NC OUTPUT NC	35								
Sled servo off control input pin.  Input pin for CLV error signal from DSP.  Input pin for CLV error signal from DSP.  Input pin for CLV error signal from DSP.  RFSM RFSM RFS- Pin for setting RF gain and EFM signal 3T compensation constant together with RFSM pin.  SLC (SLICE LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP.  Input pin for controlling data slice level by DSP.  GND pin of digital system.  For Focs Smoosing capacita output pin.  Tracking Balance control pin.  NC NO CONNECT.  Output pin for detecting disc defect.			Output pin of TES signal to DSP.						
Suc	1	1							
40 CV+ 41 RFSM RFSM RFS- 42 RFS- 43 SLC (SLICE LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP. 44 SLI Input pin for controlling data slice level by RF waveshape DSP. 45 DGND GND pin of digital system. 46 FSC Focs Smoosing capacita output pin. 47 TBC Tracking Balance control pin. 48 NC NO CONNECT. 49 DEF Output pin for detecting disc defect.	1								
41 RFSM RF output pin. 42 RFS- 43 SLC (SLICE LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP. 44 SLI Input pin for controlling data slice level by DSP. 45 DGND GND pin of digital system. 46 FSC Focs Smoosing capacita output pin. 47 TBC Tracking Balance control pin. 48 NC NO CONNECT. 49 DEF Output pin for detecting disc defect.		1							
42 RFS- 43 SLC 44 SLI 45 DGND 46 FSC 47 TBC 48 NC 49 DEF 49 DEF 40 SLI 41 Pin for setting RF gain and EFM signal 3T compensation constant together with RFSM pin. (SLICE LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP. (SLICE LEVEL CONTROL) is output pin for controlling data slice level by DSP. (GND pin of digital system. For Focs Smoosing capacita output pin. Tracking Balance control pin. NO CONNECT. Output pin for detecting disc defect.	N .								
43 SLC 44 SLI Input pin for controlling data slice level by RF waveshape DSP. 45 DGND 46 FSC 47 TBC 48 NC 49 DEF  (SLICE LEVEL CONTROL) is output pin for controlling data slice level by DSP. Input pin for controlling data slice level by DSP. GND pin of digital system. For Focs Smoosing capacita output pin. Tracking Balance control pin. NO CONNECT. Output pin for detecting disc defect.			Pin for setting RF gain and EFM signal 3T compensation constant together with RFSM pin.						
45 DGND GND pin of digital system. 46 FSC For Focs Smoosing capacita output pin. 47 TBC Tracking Balance control pin. 48 NC NO CONNECT. 49 DEF Output pin for detecting disc defect.			(SLICE LEVEL CONTROL) is output pin for controlling data slice level by RF waveshape DSP.						
46 FSC For Focs Smoosing capacita output pin. 47 TBC Tracking Balance control pin. 48 NC NO CONNECT. 49 DEF Output pin for detecting disc defect.	44	SLI							
47 TBC Tracking Balance control pin. 48 NC NO CONNECT. 49 DEF Output pin for detecting disc defect.	<b>I</b>	1							
48 NC NO CONNECT. 49 DEF Output pin for detecting disc defect.	1								
49 DEF Output pin for detecting disc defect.									
1'									
TO TO THE CARK TO DISTRIBUTE CHOCK INDUIT DITE. DOE 4.201/INDUIT.	50	CLK	Standard clock input pin. DSP4.23MHz is input.						
51 CL Micro computer command clock input pin.									
52 DAT Micro computer command data input pin.									
53 CE Micro computer command chip enable input pin.		CE							
54 DRF (DEFECT RF) RF level detecting output.									
55 FSS Focs serch select pin.									
56 VCC2 VCC pin for servo system and digital system. 57 REF1 Connection pin for standard voltage capacitor.									
58 VR Standard voltage output pin. 59 LF2 Pin for setting constant at the time of detecting disc defect.			Pin for setting constant at the time of detecting disc defect.						
60 PH1 Pin for connecting condensor for RF signal peak hold.			Pin for connecting condensor for RF signal peak hold.						
BH1 Pin for connecting condensor for RF signal bottom hold.	1	1	Pin for connecting condensor for RF signal bottom hold.						
62 LDD APC circuit output pin.	62	LDD	APC circuit output pin.						
63 LDS APC circuit input pin.	1								
64 VCC1 RF system VCC pin.	64	VCC1	HF system VCC pin.						

## IC CIRCUIT DESCRIPTION

# IC650 - LA9240MS REF APC RF DET FIN2 1 FINI 2 (3) DGND ε ③ U-COM INTER FACE BAL VCA TESI 8 T. SERVO & T. LOGIC SCH (9) SPINDLE SERVO F. SERVO B F.LOGIC SLED SERVO

IC651 - BA6999FP



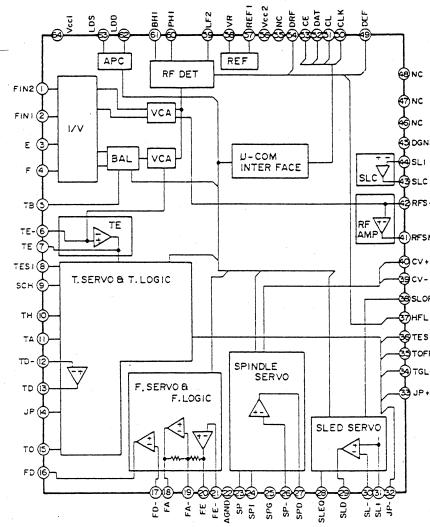
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## IC CIRCUIT BLOCK IC801 - LC66566B

PIN NO	PORT NAME
1	D7
2	D6
3	D5
3	D4
5	D3
6	D2
7	D1 -
8	DO I
9	CLAMP
10	UP
11	LOW
12 13	MAG.SW
13	DS
14	A/D
14	
15	R/W
16	N.C
17	N.C N.C
18	N C
	IN.C
19	[N.C [
20	IN.C
21	N.C INT
22	TEST
23	VSS
24	OSC1
24 25	0SC1 0SC2
26	RESET
27	
27	BATT
28	N.C
29 30	N.C N.C
30	POWER
31	N.C
32	ELV-
32 33 34	ELV+
34	FEM-
35	FEM+
36	LOAD+
37	LOAD-
38	N.C
39	N.C
40	VRO
	11/54
41	VR1
42	TH1
43	TH2
44	ACC
45	D.ON
40	F IFCT
46	EJECT
47	DRF
48	N.C
49	DISC.C
50	6/10CD
	MUTE
51	MUTE
52	DSRST
53	LOADO
54	THETPE
55	N.C
	IN. C
56	IVUU
57	SQOUT
58	COIN
59	CQCK
	INDO
I KII	WRQ
60	N.C
61	IN. C
61	RWC
61 62	RWC
61	RWC N.C STBY

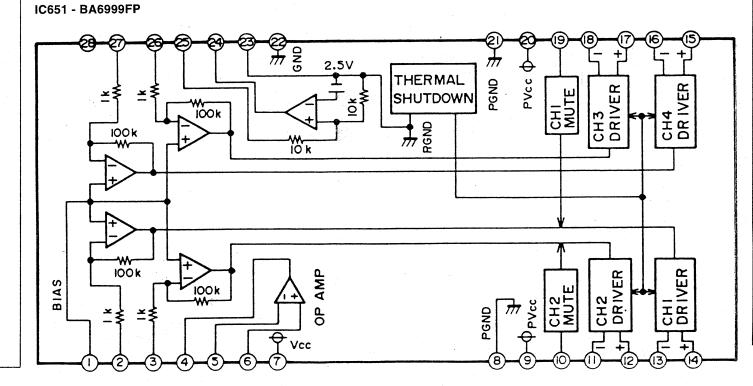
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IC650 - LA9240MS



ove mirror.

SP.



## IC CIRCUIT BLOCK DIAGRAM

IC801 - LC66566B

	PORT NAME	1/0	
	D7		8bit bi-directional address or data bus D2B
	D6	I/0	8bit bi-directional address or data bus D2B
3	D5	I/0	8bit bi-directional address or data bus D2B
4	D4	1/0	8bit bi-directional address or data bus D2B
5	D3		8bit bi-directional address or data bus D2B
	D2		8bit bi-directional address or data bus D2B
	D1 -		8bit bi-directional address or data bus   D2B
	DO		8bit bi-directional address or data bus  D2B
	CLAMP		Disc clamp finish SW input
	UP		
			Elevator upper limit SW input
	LOW		Elevator lower limit SW input
	MAG.SW		Magazine detection SW input
	DS		Data strobe to access data bus D2B
	A/D		Selects address or data on DO~D7
	R/W	0	Read/Write selector D2B
16	N.C	0	No connection
17	N.C	0	No connection
18	N.C	0	No connection
	N.C		No connection
	N.C		No connection
	INT		Interrupt input D2B
	TEST	Ī	Connection to ground
	VSS		
	0SC1		Ground
			Ceramic oscillator connection terminal for system clock
	OSC2		Ceramic oscillator connection terminal for system clock
	RESET		Reset signal input terminal
	BATT		Battery LOW detection input terminal
	N.C		No connection
	N.C		No connection
30	POWER	0	Main power supply ON/OFF output terminal
31	N.C	0	No connection
32	ELV-	0	Elevator moving-down output signal
	ELV+		Elevator moving-up output signal
	FEM-		Pickup moving-inside ouput signal
	FEM+		Pickup moving-outside output signal
	LOAD+	0	Output to move a tray in the direction out of magazine with loading motor
	LOAD-	0	Output to move a tray in the direction back to magazine with loading motor
	N.C		No connection
	N.C		No connection
	VR0	I	Reference voltage for TH1
	VR1		Reference voltage for TH2
	TH1	I	Temperature sensor ON input signal
43	TH2	I	Temperature sensor OFF input signal
	ACC		ACC ON/OFF detection input
	D.ON	I	Disc detection Photo transistor input terminal
	EJECT	I	Magazine eject key input terminal
	DRF	Ī	Focus OK signal input terminal
	N.C		No connection
	DISC.C		Elevator position detection pulse input
	6/10CD	Ī	Initial setting input terminal for 6discs or 10discs changer switching
	MUTE		Mute output terminal
	DSRST		Reset signal for DSP(LC78620E) output terminal
	LOADO		Magazine tray detection SW input
	INSIDE		Inner circle limit detection SW input
	N.C		No connection
	VDD		Power supply terminal(+5V)
	SQOUT		Interface with DSP(LC78620E)
58	COIN	0	Interface with DSP(LC78620E)
	CQCK	0	Interface with DSP(LC78620E)
59	WRQ	I	Interface with DSP(LC78620E)
59 60			No connection
59 60 61	N.C	0	No connection
59 60 61 62		0	

PCS 82 457

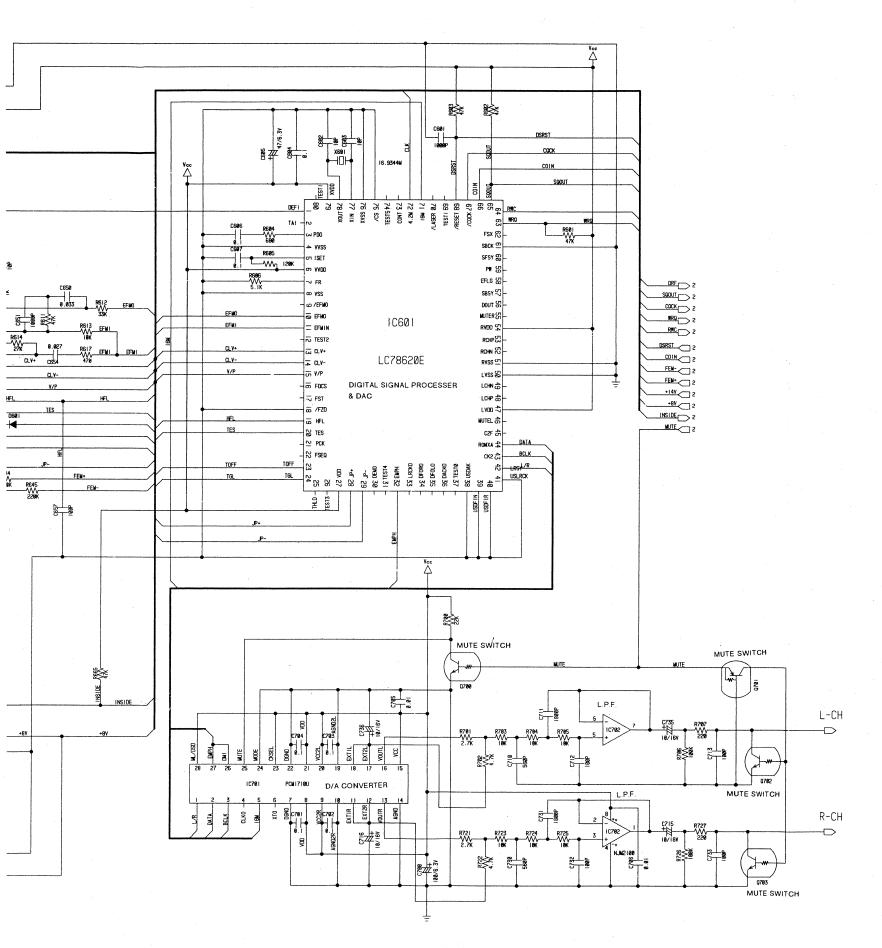
VOLTAGE (Note: all vo

IC601 - L(

٥١ 1: 2: N. 3. 3: N 5. 2. N 10: 11: 12: 13: 0' 14: 0' 15: Ν 16: 17: Ν 18-20: Ο, 21: N 22: Ν 23: 0 24: 25: 26: 27: 28: 29: 30-37: 38-41: 42-46: 47: 48: 49: 50: 51: 52: 53: 54: 55-60: 61: 62: 63-65: 66: 67: 68: 69-71: 72: 73: 74: 75: 76: 77: 78: 79:

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80:



### **VOLTAGE MEASUREMENT TABLES**

(Note: all voltages given in these tables are <u>average DC values</u>, unless otherwise noted)

(Note: a	ll voltages givei	n in these tables a	are <u>avera</u>	ge <u>DC values,</u> i	unless otherwise	noted)	
IC601 -	LC78620E		IC650 -	LA9240MS		IC701 -	PCM1710U
1:	OV		1-21:	2.5V		1-6:	5.0V CLOCK
2:	N.C.		22:	0V		7:	0V
3:	3.0V		23-31:	2.5V		8:	5.0V
4:	0.0 <b>V</b>		32:	0V		9:	5.0V 5.0V
5:	1.8V		33:	0V		9. 10:	0V
6:	5.0V		34:	5.0V		11-13:	2.5V
7:	1.5V		35:	0V	'n	14:	0V
8:	0V		36:	5.0V PULSE		15:	5.0V
9:	N.C.		37-39:	0V		16:	2.5V
10:	5.0V PULSE		40:	5.0V PULSE		17:	2.5V
11:	2.5V		41:	3.0V PULSE		18:	0V
12:	N.C.		42-44:	2.5V		19:	0V
13:	5.0V PULSE		45:	0V		20:	5.0V
14:	0V		46-48:	N.C.		21:	5.0V
15:	0V		49:	OV	. *	22:	0V
16:	N.C.		50:	5.0V CLOCK		23:	5.0V
17:	N.C.		51:	5.0V CLOCK		24:	0V
18-20:	0V		52:	5.0V 0200K		25:	5.0V
	N.C.		52. 53:	0V		25. 26:	0V
21:							
22:	N.C.		54:	5.0V		27:	0V
23:	0V		55:	N.C.		28:	5.0V
24:	5.0V		56:	5.0V			
25:	N.C.		57-60:	2.5V			
26:	N.C.		61:	2.0V		IC702 -	NJM2100M
27:	5.0V		62:	3.8V		1-3:	2.5V
28:	0V		63:	0.2V		4:	0V
29:	OV		64:	5.0V			
30-37:	N.C.					5-7:	2.5V
38-41:	0V					8:	5.0V
42-46:	N.C.						
42-40. 47:	5.0V			BA6999FP			
			1-3:	2.5V		Q601 -	2SA1338-6
48:	5.0V PULSE		4:	N.C.		B:	3.5V
49:	5.0V PULSE		5:	2.5V		C:	2.0V
50:	0V		6:	2.5V		E:	4.0V
51:	0V		7:	8.0V		<b>.</b>	4.0 V
52:	5.0V PULSE		8:	0V			
53:	5.0V PULSE	•	9:	8.0V			
54:	5.0V		10:	0V		Q700 -	DTC114TK
55-60:	N.C.		11:	0.5V		B:	0V
61:	ΟV		12-14:	0.5 <b>V</b>		C:	5.0V
62:	N.C.					E:	0V
63-65:	5.0V PULSE		15:	2.0V PULSE			
66:	5.0V		16-19:	0V			
67:	5.0V CLOCK		20:	8.0V			
68:	5.0V		21-23:	0V		Q701 -	UN2113
69-71:	N.C.		24:	N.C.		B:	0V
			25:	N.C.		C:	0V
72:	5.0V CLOCK		26:	2.5V		E:	0V
73:	N.C.		27:	2.5V			
74:	N.C.		28:	N.C.			
75:	0V						
76:	0V						DTC114TK
77:	4.2V					B:	0V
78:	5.0V CLOCK					C:	0V
79:	5.0V					E:	0V
80:	N.C.						
30.						0703 -	DTC114TK
							0V
						B:	
						C: F·	0V 0V
						⊢.	OV

## OLTAGE MEASUREMENT TABLES

C802 - S-8054HN

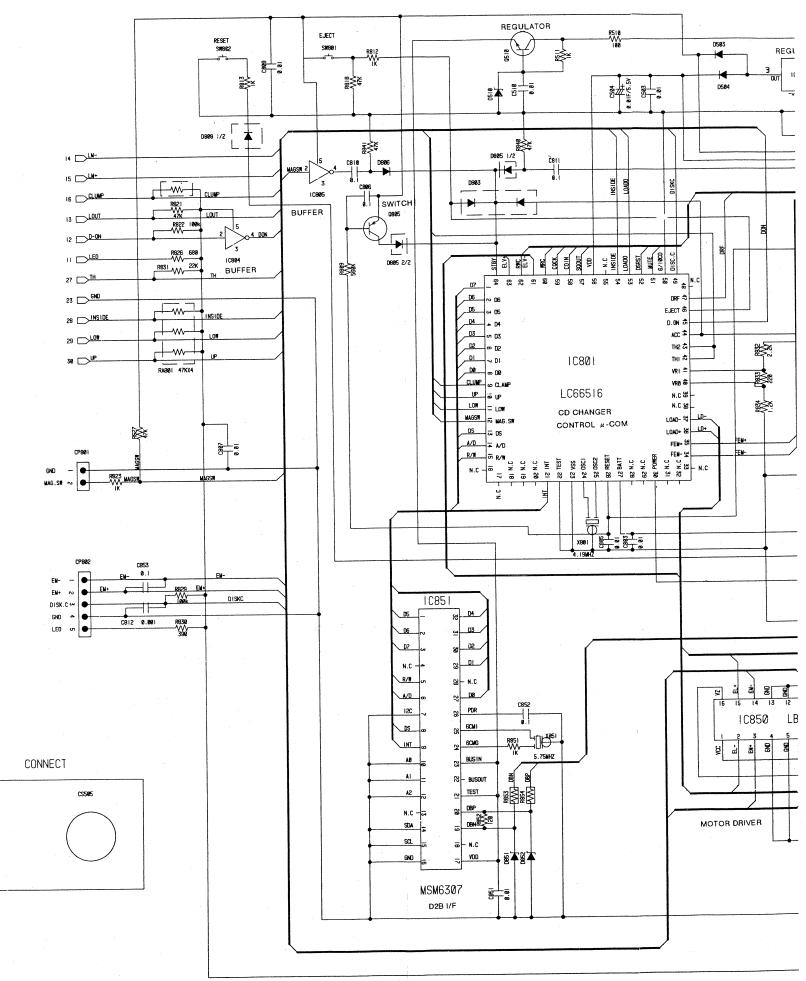
22RC026

5.0V 8.0V 0V

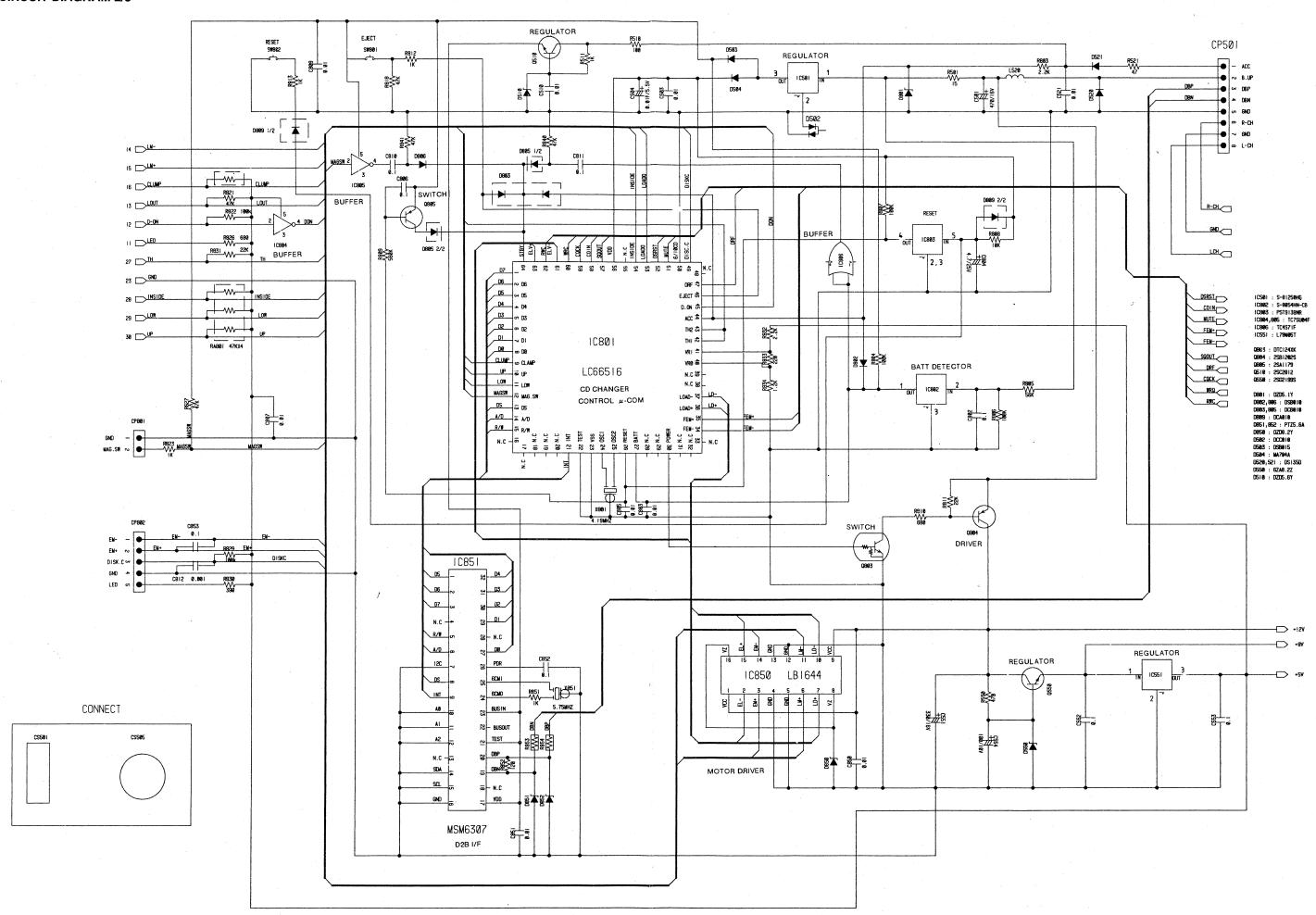
Note: all voltages given in these tables are <u>average DC values</u>, unless otherwise noted)

3501 -	MC147805AUT		IC803 -	PST9138N		21:	5.0V
	0.5V		1:	N.C.		22:	5.0V PULSE
•	12.0V		2:	0V		23:	5.0V
•	5.5V		3:	0V		24:	5.0V CLOCK
•	5.5V		4:	5.0V		25:	5.0V CLOCK
			5:	5.0V		26:	5.0V
			J.	3.0 V		27:	5.0V PULSE
C551 -	L78M05T					28:	0V
•	8.0V					29-32:	5.0V PULSE
:	0V			TC7SU04F			
: 1	5.0V		1:	0V		Q510 - 2	2SC2812
			2:	4.5V		B:	5.5V
			3:	0V		C:	12.0V
C801 -	LC66566B		4:	0V		E:	5.0V
	5.0V		5:	5.0V		L.	0.01
	5.0V						
:	0V					0.550	00001000
	5.0V		IC805 -	TC7SU04F			2SD2199S
-9:	0V		1-3:	OV		B:	8.6V
0:	5.0V		4:	5.0V		C:	12.0V
1:	5.0V		5:	5.0V		E:	8.0V
2:	0V						
3:	5.0V						
4:	0V		ICRUE -	TC4S71F		Q803 -	DTC124XK
5:	5.0V		1:	5.0V		B:	4.0V
6-20:	OV		1. 2:	5.0V 5.0V		C:	٥V
11:	5.0V		2. 3:	0V		E:	VO
2:	0V		3. 4:	5.0V			
:3:	0V		4. 5:	5.0V 5.0V			
:4:	5.0V CLOCK		5.	5.0 v		0804 -	2SB1202
 !5:	5.0V CLOCK					B:	11.0V
!6-30:	5.0V					C:	12.0V
11-39:	0V			LB1644		E:	12.0V
0:	1.8V		1:	12.0V		<b>L</b> .	12.00
1:	2.0V		2-8:	0V			
2:	4.0V		9:	12.0V			00.4470
-: 3:	4.0V		10-16:	0V			2SA1179
4:	5.0V					B:	5.0V
<b> 5</b> :	0V					ç:	0V
6:	0V		IC851	- MSM6307GS		E:	5.0V
<b>7</b> :	5.0V		1-3:	5.0V PULSE			
18:	5.0V		4:	0V			
19-51:	0V		5:	5.0V PULSE			
52-55:	5.0V		6:	5.0V PULSE			
i6:	5.0V		7:	0V			
57:	5.0V PULSE		8:	5.0V PULSE			
i8:	5.0V		9:	5.0V PULSE			
i9:	5.0V CLOCK		10-16:	0V			
30:	5.0V PULSE		17:	5.0V			
31-63:	OV		18:	OV			
34:	5.0V		19;	2.5V			
		•	20:	2.5V			

**CIRCUIT DIAGRAM 2/3** 



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